

Vishay Semiconductors

Insulated Gen 2 Schottky Rectifier Module, 100 A



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS						
I _{F(AV)} per module at T _C = 93 °C	100 A					
V_{R}	100 V					
V_{FM} at 50 A, T_C = 25 °C	0.83 V					
Package	SOT-227					
Circuit configuration	Two separate diodes, parallel pin-out					

FEATURES

- Max. T_J = 150 °C
- Two fully independent diodes
- Fully insulated package
- Trench MOS Barrier Schottky technology
- Ultra low forward voltage drop
- · Optimized for power conversion: welding and industrial SMPS applications
- · Easy to use and parallel
- Industry standard outline
- · Designed and qualified for industrial level
- UL approved file E78996



• Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

DESCRIPTION

The VS-QA100FA10 insulated modules integrate two state of the art Trench MOS Schottky technology rectifiers in the compact, industry standard SOT-227 package.

These devices are thus intended for high frequency converters and switching power supplies.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES				
V _F	I _F = 50 A, T _J = 150 °C	0.66	V			
T _J	Range	-40 to +150	°C			

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise specified)						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage		V _R		100	V	
Average forward current	per module		T _C = 93 °C	100		
Average forward current	per diode	I _{F(AV)}	T _C = 93 °C	50		
Continuous forward current	per module		T _C = 90 °C	134	Α	
Continuous forward current	per diode	I _F	T _C = 90 °C	67		
Single pulse forward current per diode		I _{FSM}	$T_C = 150 ^{\circ}\text{C}, t = 6 \text{ms}, \text{square}$	450		
Maximum power dissipation pe	er diode	P_D	T _C = 90 °C	67	W	
Non-repetitive avalanche energ	gy per diode	E _{AS}	T _J = 25 °C, L = 1 mH	583	mJ	
RMS isolation voltage		V _{ISOL}	Any terminal to case, t = 1 min	2500	V	
Operating junction and storage	temperatures	T _J , T _{Stg}		-40 to +150	°C	

ELECTRICAL SPECIFICATIONS PER DIODE (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	I _R = 1 mA	100	-	-	
Forward voltage	W	I _F = 50 A	-	0.83	1.03	V
	V_{FM}	I _F = 50 A, T _J = 150 °C	-	0.66	-	
Reverse leakage current		V _R = 100 V	-	0.03	0.8	A
	I _{RM}	T _J = 125 °C, V _R = 100 V	-	17	-	mA
Junction capacitance	C _T	V _R = 100 V, f = 1 MHz	-	259	-	pF

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THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction-to-case, single leg conducting	D		-	-	0.89	
Junction-to-case, both leg conducting	R_{thJC}		-	-	0.45	°C/W
Case-to-heatsink	R _{thCS}	Flat, greased surface	-	0.1	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				SC	T-227	

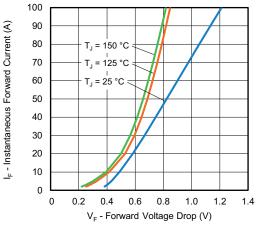


Fig. 1 - Typical Forward Voltage Drop Characteristics

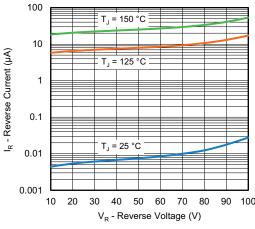


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

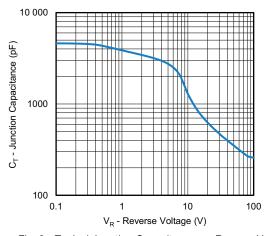


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

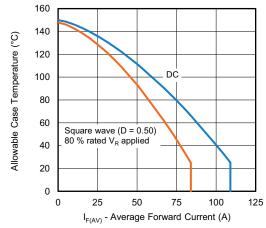


Fig. 4 - Current Rating Characteristics





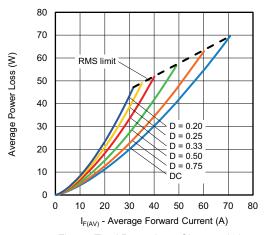


Fig. 5 - Total Power Loss Characteristics

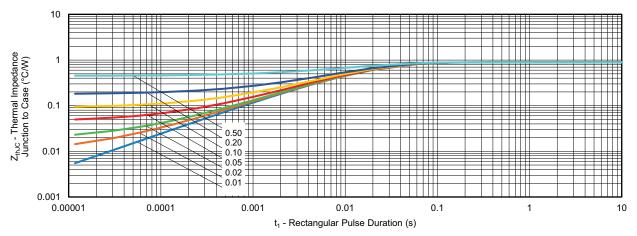
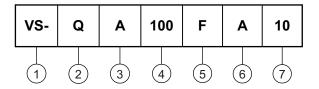


Fig. 6 - Maximum Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

Device code

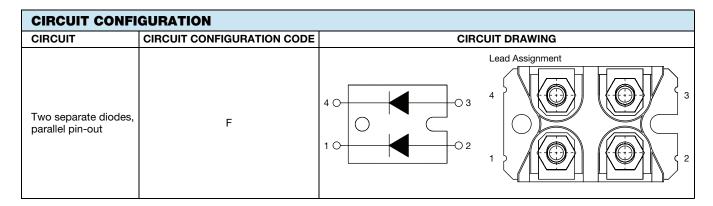


- 1 Vishay Semiconductors product
- 2 Schottky technologies
- Present silicon generation
- 4 Current rating (100 = 100 A)
- 5 Circuit configuration (two separate diodes, parallel pin-out)
- 6 Package indicator (SOT-227 standard insulated base)
- 7 Voltage rating (10 = 100 V)

Quantity per tube is 10, M4 screw and washer included



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LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95423</u>					
Part marking information	www.vishay.com/doc?95425				



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