

Vishay Dale

# Surface Mount Transformers/Inductors, Gapped and Ungapped, **Custom Configurations Available**



## **FEATURES**

 Material categorization: for definitions of compliance please www.vishav.com/doc?99912



# COMPLIANT

Inductance Range: 10  $\mu H$  to 47 000  $\mu H$ , measured at 0.10  $V_{RMS}$  at 10 kHz without DC current, using an HP 4263A or HP 4284A impedance analyzer

**HALOGEN FREE** 

**DC Resistance Range:** 0.03  $\Omega$  to 19.1  $\Omega$ , measured at

+25 °C ± 5 °C

Rated Current Range: 2.00 A to 0.09 A

**ELECTRICAL SPECIFICATIONS** 

Dielectric Withstanding Voltage: 500 V<sub>RMS</sub>, 60 Hz, 5 s

	IND.		l	DCR MAX.	MAX. RATED DC CURRENT	SATURATING CURRENT	_
MODEL	(μH)	IND. TOL.	SCHEMATIC LETTER	$(\Omega)$	(A) (1)	(A) (2)	
							_
LPE4841ER101NU LPE4841ER151NU	100 150	± 30 % ± 30 %	A	0.17 0.21	0.88 0.79	N/A	l
LPE4841ER221NU	220	± 30 % ± 30 %	A A	0.21	0.79 0.721	N/A N/A	l
LPE4841ER331NU	330	± 30 % ± 30 %	Ä	0.23	0.721	N/A N/A	3
LPE4841ER471NU	470	± 30 % ± 30 %	Ä	0.36	0.60	N/A N/A	ò
LPE4841ER681NU	680	± 30 % ± 30 %	Ä	0.36	0.60	N/A N/A	
LPE4841ER102NU	1000	± 30 %	A	0.53	0.34	N/A N/A	1200
LPE4841ER152NU	1500	± 30 % ± 30 %	Ä	0.55	0.49	N/A N/A	Ċ
LPE4841ER222NU	2200	± 30 % ± 30 %	Ä	0.63	0.45	N/A N/A	
LPE4841ER332NU	3300	± 30 %	Ä	1.55	0.40	N/A N/A	4 4 6 1 1
LPE4841ER472NU	4700	± 30 %	Ä	1.85	0.29	N/A N/A	H
LPE4841ER682NU	6800	± 30 %	Ä	4.36	0.20 0.17	N/A	i
LPE4841ER103NU	10 000	± 30 %	Ä	5.29	0.16	N/A	13
_PE4841ER153NU	15 000	± 30 %	Ä	6.48	0.14	N/A	ľ
LPE4841ER223NU	22 000	± 30 %	l A	13.1	0.10	N/A	ľ
LPE4841ER333NU	33 000	± 30 %	Ä	16.0	0.09	N/A	l
LPE4841ER473NU	47 000	± 30 %	Ä	19.1	0.08	N/A	l
LPE4841ER100MG	10	± 20 %		0.03	2.03	2.320	Γ
LPE4841ER150MG	15	± 20 %	B B C C	0.04	1.84	1.925	l
_PE4841ER220MG	22	± 20 %	С	0.07	1.32	1.610	l
LPE4841ER330MG	33	± 20 %	Č	0.09	1.20	1.330	١,
LPE4841ER470MG	47	± 20 %	D	0.13	0.98	1.125	į
LPE4841ER680MG	68	± 20 %	D	0.21	0.79	0.941	ľ
LPE4841ER101MG	100	± 20 %	E	0.35	0.58	0.781	H
LPE4841ER151MG	150	± 20 %	E	0.48	0.52	0.641	H
_PE4841ER221MG	220	± 20 %	E	0.73	0.42	0.532	
LPE4841ER331MG	330	± 20 %	E	1.14	0.34	0.436	
LPE4841ER471MG	470	± 20 %	E E E E	1.36	0.31	0.366	li
LPE4841ER681MG	680	± 20 %		2.07	0.25	0.305	ı
LPE4841ER102MG	1000	± 20 %	E E E	3.15	0.20	0.252	
LPE4841ER152MG	1500	± 20 %	E	4.76	0.16	0.206	۱ (
LPE4841ER222MG	2200	± 20 %	<u>E</u>	7.29	0.13	0.170	l
LPE4841ER332MG	3300	± 20 %	<u>E</u>	11.7	0.11	0.139	l
LPE4841ER472MG	4700	± 20 %	E	17.7	0.09	0.117	ı

DESCRIPTION								
LPE	4841	1000 μH	± 30 %	Α	ER		e2	
MODEL	SIZE	INDUCTANCE VALUE	INDUCTANCE TOLERAI	NCE CORE PAG	CKAGE CODE	JEDEC LEAD	(Pb)-FREE	STANDARD
GLOBAL PART NUMBER								
I	L	P E 4	8 4 1	E R	1 0	2	N	С
P	RODUC	CT FAMILY	SIZE	PACKAGE CODE	INDUCTANC	E VALUE	TOL.	CORE

Note

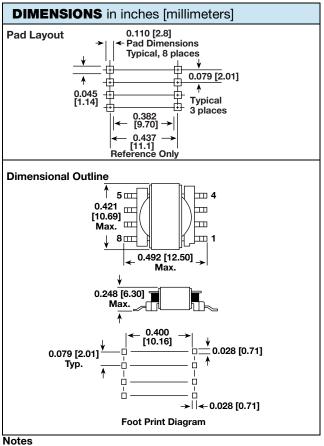
DC current that will create a maximum temperature rise of 30 °C when applied at +25 °C ambient DC current that will typically reduce the initial inductance by 20 % UNGAPPED MODELS: Highest possible inductance with the lowest DCR and highest Q capability. Beneficial in filter, impedance matching and line coupling devices

GAPPED MODELS: Capable of handling large amounts of DC current, tighter inductance tolerance with better temperature stability than ungapped models. Beneficial in DC/DC converters or other circuits carrying DC currents or requiring inductance stability over a temperature range

Series is also available with SnPb terminations by using package code RY for tape and reel (in place of ER) or SM for bulk (in place of EB)



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- Pad layout guidelines per MIL-STD-275E (printed wiring for electronic equipment)
- Tolerances:  $xx \pm 0.01$ " [ $\pm 0.25$  mm];  $xxx \pm 0.005$ " [ $\pm 0.12$  mm]
- The underside of these components contains metal and thus should not come in contact with active circuit traces

SCHEMATIC (top view)						
Schen	natic A	Schei	matic B	Schematic C		
5 ®	_⊙ 4	5 ⊕_¯¯¯¯	~~~° 4	5 ⊕	0 4	
6 ©	€ ⊚ <b>3</b>	6 ⊕-~~	~~~ം ₃	60	_ ⊚ 3	
7 ©		7 9	~~~°₀ 2	70	° 2	
8 ©	"`1	8 9	~~~⁰₁	8 0	° 1	
	Schematic D			Schematic E		
5 ⊕		⊕ 4	5 ⊕	0 4		
6 ⊕		⊕ 3	6 ⊖	⊕ 3		
7 ⊕—		<b>2</b>	7 0	<sup>®</sup> 2		
8 9-	~~~~~	<b>№</b> 1	8 •—			

#### Note

· Schematic A is for ungapped LPE series

ENVIRONMENTAL PERFORMANCE			
TEST	CONDITIONS		
Thermal cycling	Withstands -55 °C to +125 °C		
Operating temperature	-55 °C to +125 °C <sup>(1)</sup>		
High humidity	85 %		
Soldering heat	Tested to +230 °C		
Mechanical shock	Per MIL-STD-202, method 213 (100G)		
Vibration	Per MIL-STD-202, method 204 (20G)		
Solderability	Per industry standards		

### Note

(1) Must be checked in end use application

## **PART MARKING**

- Vishay Dale
- Date code
- Marking code (suffix of model #)
- Pin 1 indicator

# **PACKAGING**

# **TAPE SPECIFICATIONS:**

Carrier tape type: conductive Cover tape type: anti-static

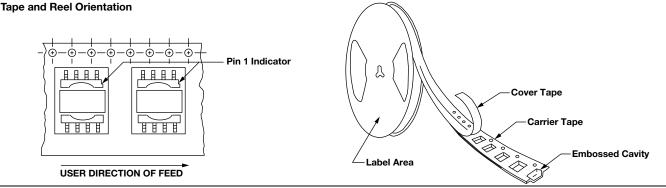
Cover tape adhesion to carrier: 40 g ± 30 g

# **REEL SPECIFICATIONS:**

Diameter (flange): 13" [330.2 mm] Maximum width (over flanges): 1.197" [30.4 mm]

STANDARDS: All embossed carrier tape packaging will be accomplished in compliance with latest revision of EIA-481 "Taping of Surface Mount Components for Automatic Placement"

MODEL	TAPE WIDTH	COMPONENT PITCH	UNITS PER 13" REEL
LPE-4841	24 mm	16 mm	600



## Note

Top view shown with cover tape removed



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