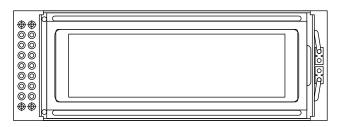
## LCD-122H032O



Vishay

## 122 x 32 Graphic LCD



MECHANICAL DATA					
ITEM	STANDARD VALUE	UNIT			
Module dimension	77.8 x 27.2				
Viewing area	60.0 x 18.0				
Dot size	0.40 x 0.45				
Dot pitch	0.44 x 0.49	mm			
Mounting hole	n/a				
Character size	n/a				

## **FEATURES**

- Type: graphic
- Display format: 122 x 32 dots
- Built-in controller: SBN1661G
- Duty cycle: 1/32
- N.V. optional for +3 V power supply
- LED backlight only white version
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

ABSOLUTE MAXIMUM RATINGS						
ITEM	SYMBOL	STAN	UNIT			
	STIVIDUL	MIN.	TYP.	MAX.		
Power supply	$V_{\text{DD}}$ to $V_{\text{SS}}$	4.75	5.0	5.25	v	
Input voltage	VI	0	-	$V_{DD}$		

Note

•  $V_{SS} = 0 V, V_{DD} = 5.0 V$ 

ELECTRICAL CHARACTERISTICS							
ITEM	SYMBOL CONDITI		ST	STANDARD VALUE			
		CONDITION	MIN.	TYP.	MAX.	UNIT	
Input voltage	V <sub>DD</sub>	-	4.5	5.0	5.5	V	
Supply current	I <sub>DD</sub>	-	-	1.0	-	mA	
Recommended LC	$V_{DD}$ to $V_0$	-20 °C	-	-	5.8	V	
driving voltage for normal temperature version module		25 °C	-	4.9	-		
		70 °C	4.6	-	-		
CCFL starting voltage	V <sub>FLS</sub>	25 °C		-	V <sub>RMS</sub>		
CCFL driving voltage	V <sub>FLD</sub>	25 °C		-	V <sub>RMS</sub>		
CCFL driving current	I <sub>FLD</sub>	V <sub>FQ</sub> = 450 V <sub>RMS</sub> , 30 kHz		-	mA <sub>RMS</sub>		
LED forward voltage	V <sub>F</sub>	25 °C 3.		3.5	3.6	V	
LED forward current	I <sub>F</sub>	2 °C 32 40 60		mA			
EL power supply current	I <sub>EF</sub>	V <sub>EL</sub> = 110 V <sub>AC</sub> , 400 Hz 5.0		mA			

OPTIONS									
PROCESS COLOR				BACKLIGHT					
TN	STN GRAY	STN YELLOW	STN BLUE	FSTN B&W	STN COLOR	NONE	LED	EL	CCFL
-	х	х	-	х	-	х	х	х	-

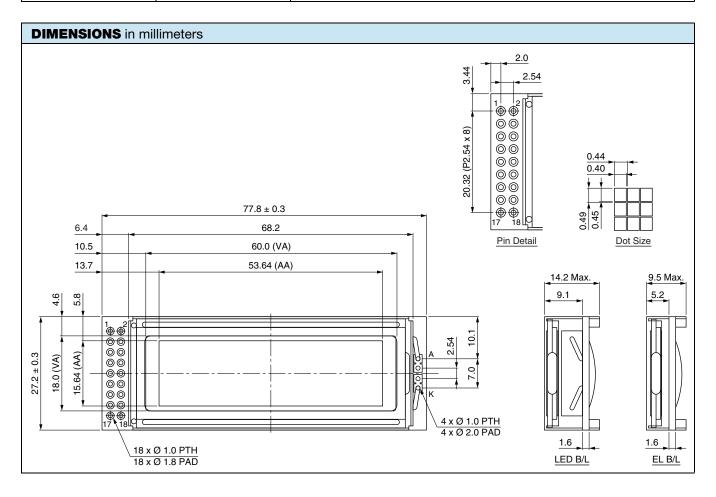
For detailed information, please see the "Product Numbering System" document.

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INTERFACE PIN FUNCTION					
PIN NO.	SYMBOL	FUNCTION			
1	V <sub>SS</sub>	Ground			
2	V <sub>DD</sub>	Supply voltage for logic			
3	V <sub>0</sub>	Operating voltage for LCD			
4	A <sub>0</sub>	H: data / L: instruction			
5	CS1	Chip select signal for IC1			
6	CS2	Chip select signal for IC2			
7	NC / CL	No connection / external clock 2 kHz			
8	NC / E	No connection / enable signal			
9	R / W	H: read data / L: write data			
10	DB0	Data bus line			
11	DB1	Data bus line			
12	DB2	Data bus line			
13	DB3	Data bus line			
14	DB4	Data bus line			
15	DB5	Data bus line			
16	DB6	Data bus line			
17	DB7	Data bus line			
18	RST	$\text{H} \rightarrow \text{L}$ : the LCM be reset			



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