

MULTILAYER CERAMIC CHIP CAPACITORS

VJ HIFREQ HT

Surface-Mount Multilayer Ceramic Chip Capacitors for High Temperatures Up to 200 °C



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KEY BENEFITS

- Case sizes: 0402, 0603, 0805, and 1111
- High frequency / high temperature to 200 °C
- Ultra stable dielectric material
- Non-magnetic copper termination, code "C"
- Lead (Pb)-free terminations code, "X"
- Tin / lead termination code, "L"
- Epoxy for conductive adhesive mounting code, "E"

APPLICATIONS

- RF and microwave
- Broadband communication
- Satellite communication
- Base stations

Medical and test instrumentationMilitary devices (radar,

Surface-mount, wet build process

Reliable Noble Metal Electrode (NME) system

Made with a combination of design, materials,

and tight process control to achieve very high

- communication, etc.)
- Wireless devices

field reliability

RESOURCES

- Datasheet: VJ HIFREQ HT <u>www.vishay.com/doc?45239</u>
- For technical questions contact <u>MLCC@vishay.com</u>
- Material categorization: for definitions please see <u>www.vishay.com/doc?99912</u>





TECHNOLOGY VJ HIFREO HT

Surface-Mount Multilayer Ceramic Chip Capacitors for High Temperatures Up to 200 °C

ELECTRICAL SPECIFICATIONS

Note

• Electrical characteristics at 25 °C unless otherwise specified

Operating Temperature: -55 °C to +200 °C

Capacitance Range:

0402: 0.1 pF to 47 pF

0603: 0.1 pF to 270 pF

0805: 0.1 pF to 1000 pF

1111: 0.2 pF to 3300 pF

Voltage Rating: 25 V_{DC} to 500 V_{DC}

Temperature Coefficient of Capacitance (TCC):

C0G (D): 0 ppm/°C ± 30 ppm/°C from -55 °C to +200 °C

Dissipation Factor (DF):

C0G (D): 0.05 % max. at 1.0 V_{BMS} and 1 MHz for values $\leq 1000 \text{ pF}$

COG (D): 0.05 % max. at 1.0 V_{RMS} and 1 kHz for values > 1000 pF

Aging Rate: 0 % maximum per decade

Insulation Resistance (IR):

at +25 °C and rated voltage 100 000 M Ω minimum or 1000 Ω F. whichever is less

at +200 °C and rated voltage 10 000 M Ω minimum or 100 Ω F, whichever is less

Dielectric Strength Test:

performed per method 103 of EIA-198-2-E.

Applied test voltages:

 \leq 100 V_{DC}-rated: min. 250 % of rated voltage

QUICK REFERENCE DATA										
	CASE	MAXIMUM VOLTAGE (V)	CAPACITANCE							
DILLEGTRIC			MINIMUM	MAXIMUM						
	0402	50	0.1 pF	47 pF						
	0603	200	0.1 pF	270 pF						
	0805	250	0.1 pF	1.0 nF						
	1111	500	0.2 pF	3.3 nF						

	ORDERING INFORMATION													
ĺ	VJ0805	D	2R2	V	X	Α	Α	С	HT					
15-May-17	VJ0805 CASE CODE I 0403 0805 1111		2R2 CAPACITANCE NOMINAL CODE L Expressed in picofarads (pF). The first two digits are significant, the third is a multiplier. An "R" indicates a decimal point. Examples: 1R0 = 1.0 pF	V CAPACITANCE TOLERANCE I V = $\pm 0.05 \text{ pF}$ B = $\pm 0.10 \text{ pF}$ C = $\pm 0.25 \text{ pF}$ D = $\pm 0.50 \text{ pF}$ F = $\pm 1\%$ G = $\pm 2\%$ K = $\pm 10\%$ M = $\pm 20\%$ Note: for details see "Selection Chart"	X TERMINATION C = non-magnetic copper barrier 100 % tin plate matte finish E = AgPd ⁽²⁾ X = Ni barrier 100 % tin plate matte finish L = Ni barrier with lead plated finish min. 4 % lead	A DC VOLTAGE RATING ⁽¹⁾ J = 16 V X = 25 V A = 50 V B = 100 V C = 200 V P = 250 V D = 300 V E = 500 V	A MARKING A = unmarked Q = marked C = 7" reel / C = 7" reel / G = 7" reel / flar J = 7" reel (flar B = 11 1/4 paper I = 11 1/4 flarmed pa B = 1 No	C PACKAGING plastic tape paper tape ned paper tape ow quantity) / 13" reel / tape / 13" reel / tape	HT PROCESS CODE					
Hevisio							terminati	on code						

Notes

- ⁽¹⁾ DC voltage rating should not be exceeded in application
- ⁽²⁾ Termination code "E" is for conductive epoxy assembly

PRODUCT SHEET

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