

MULTILAYER CERAMIC CHIP CAPACITORS



VJ HIGHFREQ Series

Surface-Mount Multilayer Ceramic Chip Capacitors for High-Frequency Applications



KEY BENEFITS

- Low dissipation factor (≤ 0.05 %)
- High Q (≥ 2000)
- Tight tolerance: as tight as ± 0.1 pF
- Broad working voltages
 - 0402: 25 V to 200 V
 - 0603: 25 V to 250 V
 - 0805: 25 V to 250 V
- Wide capacitance range: 1 pF to 1.5 nF

RESOURCES

- Datasheet: VJ HIGHFREQ Series http://www.vishay.com/doc?45071
- For technical questions contact <u>mlccrf@vishay.com</u>
- Material categorization: For definitions of compliance please see <u>http://www.vishay.com/doc?99912</u>

*Lead (Pb)-containing terminations are not RoHS-compliant. Exemptions may apply.







PRODUCT SHEET

APPLICATIONS

- Filter networks
- Matching networks
- Amplifier circuits
- DC blocking circuits

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ELECTRICAL SPECIFICATIONS

Note

• Electrical characteristics at 25 °C unless otherwise specified

Operating Temperature: - 55 °C to + 125 °C

Capacitance Range:

0402: 1.0 pF to 82 pF 0603: 1.0 pF to 470 pF

0805: 1.0 pF to 1.5 nF

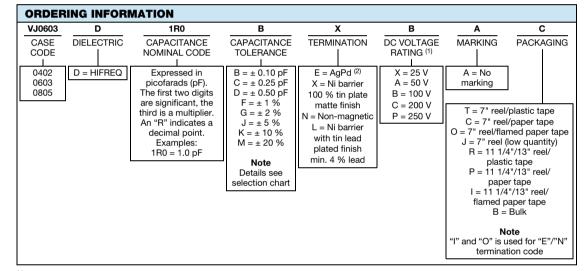
Voltage Rating: 25 V_{DC} to 250 V_{DC}

Temperature Coefficient of Capacitance (TCC):

C0G (D): 0 ppm/°C \pm 30 ppm/°C from - 55 °C to + 125 °C with zero (0) V_{DC} applied

Dissipation Factor (DF):

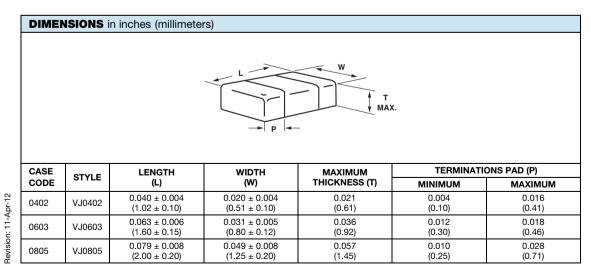
C0G (D): 0.05 % max. at 1.0 V_{RMS} and 1 MHz



Notes

⁽¹⁾ DC voltage rating should not be exceeded in application

⁽²⁾ Termination code "E" is for conductive epoxy assembly



PRODUCT SHEET

VMN-PT0320-1205