



Si5504BDC vs. Si5504DC

Description: N- and P-Channel 30-V (D-S) MOSFET  
 Package: 1206-8 ChipFET®  
 Pin Out: Identical

Part Number Replacements  
 Si5504BDC-T1-E3 Replaces Si5504DC-T1-E3  
 Si5504BDC-T1-E3 Replaces Si5504DC-T1

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)					
Parameter	Symbol		Si5504BDC	Si5504DC	Unit
Drain-Source Voltage	V <sub>DS</sub>	N-Ch	30	30	V
		P-Ch	- 30	- 30	
Gate-Source Voltage	V <sub>GS</sub>	N-Ch	± 20	± 20	V
		P-Ch			
Continuous Drain Current	I <sub>D</sub>	T <sub>A</sub> = 25 °C	N-Ch	3.7	3.9
		T <sub>A</sub> = 85 °C	P-Ch	- 2.5	- 2.8
	I <sub>D</sub>	N-Ch	2.6	2.8	
		P-Ch	- 1.8	- 2.0	
Pulsed Drain Current	I <sub>DM</sub>	N-Ch	10	10	A
		P-Ch	- 10	- 10	
Continuous Source Current (MOSFET Diode Conduction)	I <sub>S</sub>	N-Ch	1.3	1.8	A
		P-Ch	- 1.3	- 1.8	
Power Dissipation	P <sub>D</sub>	T <sub>A</sub> = 25 °C	1.5	2.1	W
		T <sub>A</sub> = 85 °C	0.8	1.1	
Operating Junction and Storage Temperature Range		T <sub>J</sub> and T <sub>stg</sub>	- 55 to 150	- 55 to 150	°C
Maximum Junction-to-Ambient		R <sub>thJA</sub>	85	60	°C/W

SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)									
Parameter	Symbol	Si5504BDC			Si5504DC			Unit	
		Min	Typ	Max	Min	Typ	Max		
<b>Static</b>									
Gate-Threshold Voltage	V <sub>GS(th)</sub>	N-Ch	1.5		3	1.0		NS <sup>a</sup>	V
		P-Ch	- 1.5		- 3	- 1.0		NS <sup>a</sup>	
Gate-Body Leakage	I <sub>GSS</sub>	N-Ch			± 100			± 100	nA
		P-Ch			± 100			± 100	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	N-Ch			1			1	µA
		P-Ch			- 1			- 1	
On-State Drain Current	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	N-Ch	10			10		A
		V <sub>GS</sub> = - 10 V	P-Ch	- 10			- 10		
Drain-Source On-Resistance	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	N-Ch	0.053	0.065		0.072	0.085	Ω
		V <sub>GS</sub> = - 10 V	P-Ch	0.112	0.140		0.137	0.165	
		V <sub>GS</sub> = 4.5 V	N-Ch	0.081	0.100		0.120	0.143	
		V <sub>GS</sub> = - 4.5 V	P-Ch	0.188	0.235		0.240	0.290	
Forward Transconductance	g <sub>fs</sub>	N-Ch		5			6		S
		P-Ch		3.5			3		
Diode Forward Voltage	V <sub>SD</sub>	N-Ch		0.8	1.2		0.8	1.2	V
		P-Ch		- 0.8	- 1.2		- 0.8	- 1.2	
<b>Dynamic</b>									
Total Gate Charge	Q <sub>g</sub>	N-Ch		4.5	7		5	7.5	nC
		P-Ch		4.5	7		5.5	6.6	
Gate-Source Charge	Q <sub>gs</sub>	N-Ch		0.7			0.8		nC
		P-Ch		0.7			1.2		
Gate-Drain Charge	Q <sub>gd</sub>	N-Ch		0.7			1.0		nC
		P-Ch		1			0.9		
Gate Resistance	R <sub>g</sub>	N-Ch		3			NS		Ω
		P-Ch		13			NS		

Notes:

a. NS denotes not specified in original datasheet.

Specification comparisons are supplied as a courtesy to compare two devices and do not constitute a commercial product datasheet or any guarantee of identical performance. Designers should refer to the appropriate datasheets of the same number for guaranteed specification limits.