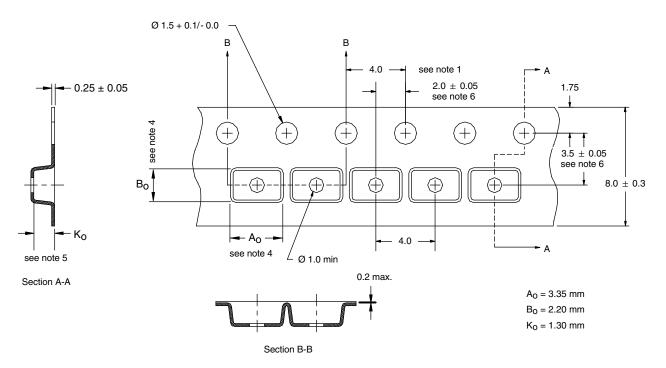


Vishay Siliconix

1206-8 ChipFET®



Notes

- ⁽¹⁾ 10 sprocket hole pitch cumulative tolerance \pm 0.2.
- ⁽²⁾ Camber not to exceed 1 mm in 100 mm, also not to exceed 1.5 cm in 1 m actually.
- ⁽³⁾ Material: Black conductive or black static dissipative.
- $^{(4)}$ A₀ and B₀ measured on a plane 0.3 mm above the bottom of the pocket.
- $^{(5)}$ K₀ measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
- ⁽⁶⁾ It should be measured from:
 - a. sprocket hole to pocket center and
 - b. sprocket hole to pocket hole
- ⁽⁷⁾ All sizes in mm unless specified.
- ⁽⁸⁾ Tolerances will be ± 0.1 mm unless specified.
- ⁽⁹⁾ Vishay part number must be labeled at all reels of carrier tape.
- ⁽¹⁰⁾ Surface resistivity: 10^4 to $10^{11} \Omega$.

QUANTITY PER REEL	
T1	3000
T3 ⁽¹⁾	10 000

Note

⁽¹⁾ Not standard offering. Please contact local sales office for availability.

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