



P-Channel 20-V (D-S), 1.5-V (G-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)		
- 20	0.024 at V _{GS} = - 4.5 V	- 7		
	0.030 at V _{GS} = - 2.5 V	- 6.2		
	0.038 at V _{GS} = - 1.8 V	- 5.2		
	0.048 at V _{GS} = - 1.5 V	- 5.0		

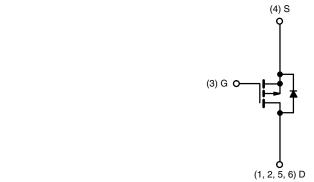
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET: 1.5 V Rated
- Ultra-Low On-Resistance
- 100 % R_q Tested
- Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

· Load Switch and PA Switch for Portable Devices



P-Channel MOSFET

		Top Vi			
T		1	6		
3 mm	Ш	2	5		
		3	4		
2.85 mm —					

Ordering Information: Si3495DV-T1-E3 (Lead (Pb)-free)

Si3495DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

Marking Code: 95xxx

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 20		V	
Gate-Source Voltage		V _{GS}	± 5			
Continuous Ducis Comment /T 450 0008	T _A = 25 °C	I_	- 7	- 5.3		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C	- I _D	- 3.6	- 3.9	۸	
Pulsed Drain Current		I _{DM}	- 20		Α	
Continuous Source Current (Diode Conduction) ^a		I _S	- 1.7	- 0.9		
Mariana Barra Biraina kina	T _A = 25 °C	P _D	2.0	1.1	W	
Maximum Power Dissipation ^a	T _A = 85 °C	' D	1.0	0.6	vV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marrian de Analian to	t ≤ 5 s	R _{thJA}	45	62.5	°C/W
Maximum Junction-to-Ambient ^a	Steady State	□thJA	90	110	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	25	30	

Note:

a. Surface Mounted on 1" x 1" FR4 board.

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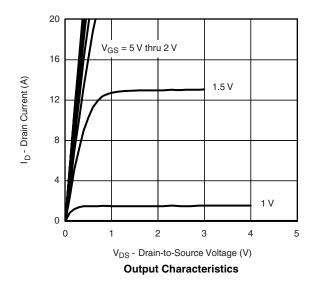
SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions Min.			Max.	Unit		
Static								
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.35		- 0.75	V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 5 \text{ V}$			± 100	nA		
7 0	1	V _{DS} = - 20 V, V _{GS} = 0 V			- 1			
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 20 V, V _{GS} = 0 V, T _J = 85 °C			- 10	μΑ		
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 4.5 V	- 20			Α		
		V _{GS} = - 4.5 V, I _D = - 7 A		0.020	0.024			
	D	V _{GS} = - 2.5 V, I _D = - 6.2 A		0.024	0.030			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 1.8 V, I _D = - 5.2 A		0.030	0.038	Ω		
		V _{GS} = - 1.5 V, I _D = - 3 A		0.036	0.048			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 7 A		25		S		
Diode Forward Voltage ^a	V_{SD}	$I_S = -1.7 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.62	- 1.1	V		
Dynamic ^b								
Total Gate Charge	Q_g			25	38			
Gate-Source Charge	Q _{gs}	V_{DS} = - 10 V, V_{GS} = - 4.5 V, I_D = - 7 A		2.5		nC		
Gate-Drain Charge	Q_{gd}			7				
Gate Resistance	R_{g}		4	8.5	13	Ω		
Turn-On Delay Time	t _{d(on)}			19	30			
Rise Time	t _r	V_{DD} = - 10 V, R_L = 10 Ω		36	55			
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 1 A, V_{GEN} = - 4.5 V, R_g = 6 Ω		200	300	ns		
Fall Time	t _f			106	160			
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.7 A, dI/dt = 100 A/μs		35	60			

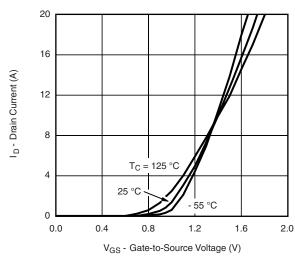
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



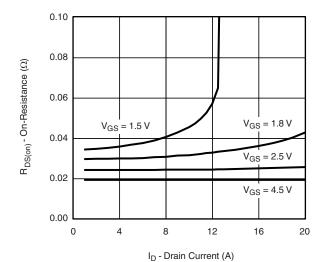




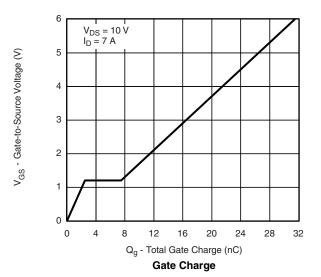




TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

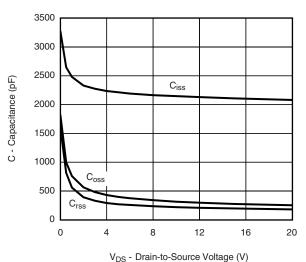


On-Resistance vs. Drain Current



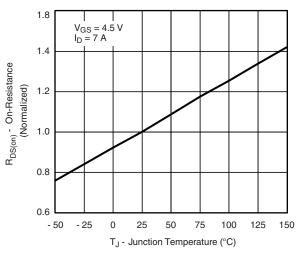
20 10 T_J = 150 °C T_J = 25 °C 1 0.1 0.0 0.2 0.4 0.6 0.8 1.0 1.2 V_{SD} - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage

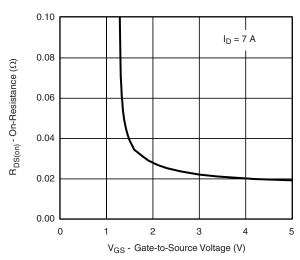


VDS - Dialin-to-Source voltage (v)





On-Resistance vs. Junction Temperature



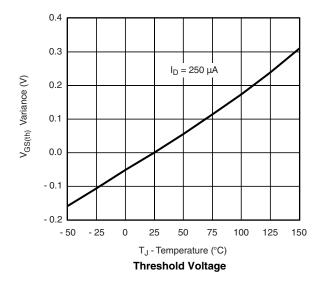
On-Resistance vs. Gate-to-Source Voltage

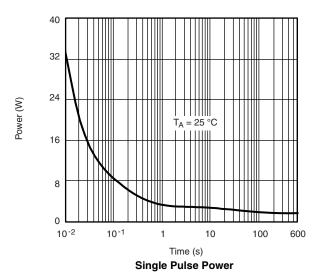
Is - Source Current (A)

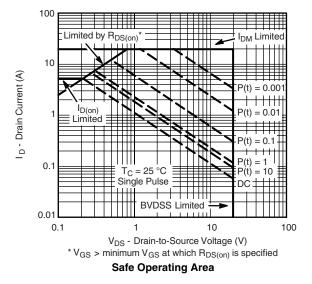
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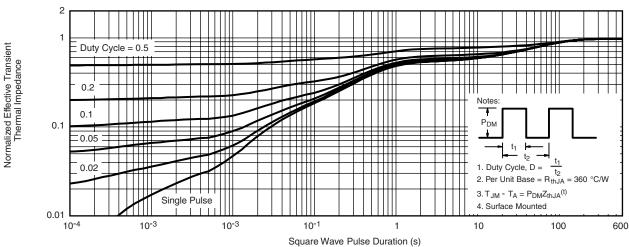
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



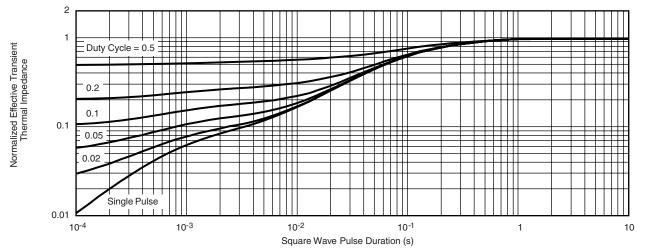






Normalized Thermal Transient Impedance, Junction-to-Ambient

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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