Application Overview

COMPATIBILITY TO EXISTING APPLICATIONS

Normally, Vishay IR receiver modules are used in systems in which the data format and the decoding software are already specified by the customer. The TSOP receiver modules will in most cases work correctly the first time they are "dropped" into the system.

In the event the receiver module does not operate as well as expected, the following items should be checked:

- The parts table, which can be found on the first page of the IR-receiver datasheets lists the most popular IR remote control data formats and the IR receiver types suitable for receiving them. If a data format is not mentioned, then carrier frequency, burst length, and repetition time of the data signal should be cross checked against the receiver type. This can be accomplished by checking the maximum envelope duty cycle diagram in the datasheet. The envelope duty cycle is defined by the burst length divided by the repetition time. In more complex IR codes all the burst durations in a data word can be summed up and divided by the data word repetition time as illustrated in Fig. 1. Accepted signals must lie below the maximum envelope duty cycle curve. If there is uncertainty regarding the selection of the type, we recommend the general purpose TSOP13438 series or contact the technical support IRR@vishay.com.
- Possible disturbance sources (ambient light, EMI, noise or ripple on the power supply) are described in the document "Disturbance Sources" (www.vishay.com/doc?80072)
- Attenuation due to an optical window in front of the sensitive area of the receiver or due to light guide coupling
- Output pulse timing tolerances of the decoding software

OUTPUT PULSE WIDTH TOLERANCES

The decoding software must accept and evaluate the output pulses of the IR receiver. In Fig. 2, there is example data of the output pulse width versus the optical input irradiance. This diagram also gives an indication of the output pulse width jitter (the difference between the min. pulse width and the max. pulse width at a given irradiance).

The tolerances of the output pulse width (t_{po}) with respect to the input burst length (t_{pi}) is given in the expression shown in Fig. 2, "Output Delay and Pulse Width" on the datasheets, for example:

$$t_{pi} - 3/f_0 < t_{po} < t_{pi} + 3.5/f_0$$

This tolerance includes variations over the entire range of temperature, supply voltage, irradiance and jitter. The jitter alone (output pulse width variation during the transmission of a data command) is much less than the above tolerances. Typical figures for the jitter are shown in Fig. 2, where the difference between maximum and minimum pulse width is calculated for each irradiance value.

If there is a decoding software compatibility problem because of the output pulse voltage level or the output pulse switching time, then an external pull up resistor (10 kΩ, see Fig. 7 in the document "Disturbance Sources" (www.vishay.com/doc?80072)) may solve the problem.

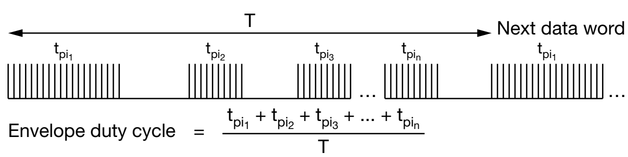


Fig. 1 - Definition of the Envelope Duty Cycle

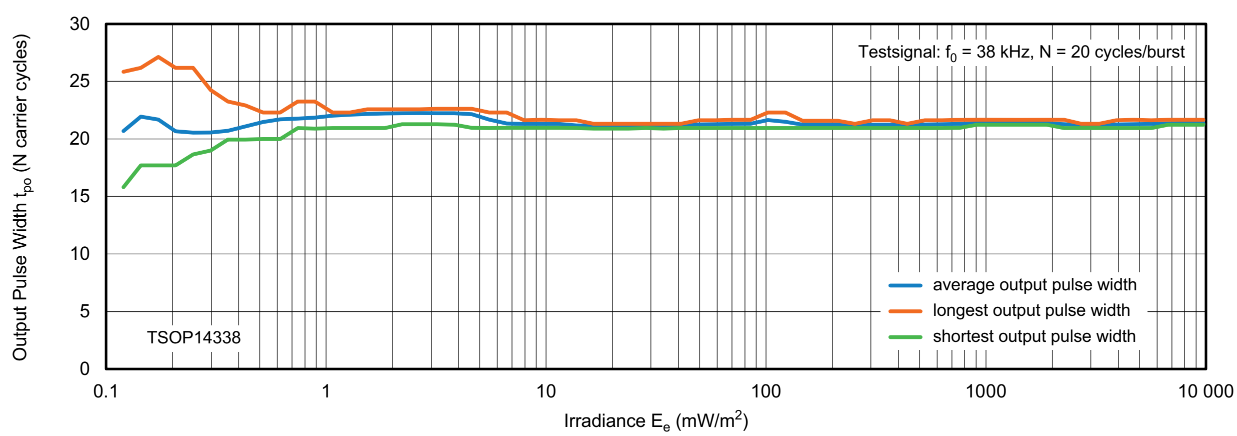


Fig. 2 - Statistical Evaluation of 1000 Output Pulses at Each Irradiance

APPLICATION CIRCUIT FOR OPERATION IN HARSH ENVIRONMENTS

The Vishay IR receivers include an efficient protection circuitry against electrostatic discharge (ESD) or electrical overstress (EOS), which is sufficient for normal handling and assembly procedures according the common industry standards.

In case of serious over-voltage-transients it might be useful to add components for a further improvement of the protection.

If the robustness of the IR receiver for an air discharge ESD test needs to be improved an additional metal holder that is electrically connected to GND (e.g. TSOP93338CA1) can act like a lightning conductor to protect the IR receiver.

Fig. 3 shows where resistors can be integrated in the circuit to improve the robustness against any overvoltage that might happen after soldering by PCB handling, PCB testing or during operation in the application (e.g. spikes from dimmer or motor control circuits).

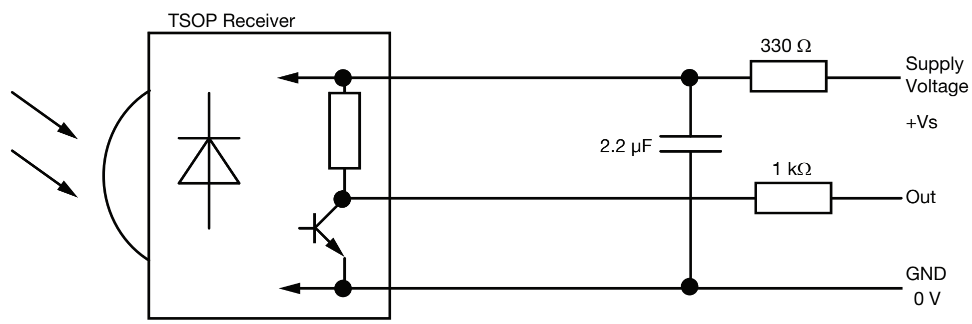


Fig. 3 - Protection Circuit Against Over Voltage Spikes

REFLOW AND PIN-IN-PASTE SOLDERING

In addition to wave soldering, the Minimold package has been qualified for reflow soldering if MSL4 baking procedures are followed. Vishay offers two configurations that take advantage of this capability, allowing the parts to be soldered in the same step as the other SMT parts on the board.

The first configuration is a top-view DF1 bending, which may be reflow soldered directly to pads on the top of the board.

The second configuration is a specially designed metal holder which allows reflow soldering via the pin-in-paste soldering technique. This holder is available for two standard PCB thicknesses, the P10 for 1.0 mm board and the P16 for 1.6 mm board. The holder itself should be soldered to GND which enhances the protection against EMI.

Fig. 4 - DF1 Bending

Fig. 5 - P10 and P16 Metal Holder for Pin-In-Paste Soldering

FIX GAIN RECEIVER FOR SENSOR APPLICATIONS

For many optical sensors it is sufficient to provide just a simple digital state like “reflection-yes” or “reflection-no” or whether a beam is interrupted. In this kind of applications the “fix gain receivers”, such as TSSP94038 and TSSP96038, are often a good solution. There is no restriction regarding the fastest burst repetition rate, it can even work with a continuous carrier signal (e.g. a continuous 38 kHz signal). The reaction time is therefore much faster and the circuit becomes simpler.

A further problem of the standard IR receivers in sensor applications is the variable detection threshold. Standard IR receivers adjust their detection threshold depending on the amount of ambient light and optical noise present in the environment in order to avoid emission of spurious pulses. In a sensor application, the power of the emitter is normally adjusted according to the maximum brightness level of the light barrier environment, which corresponds to the lowest gain of the IR receiver. However, when the IR receiver is then subjected to lower light levels, the AGC adjusts the gain and the receiver becomes too sensitive and even detects reflected or stray light.

With a fixed gain, the detection threshold and resulting detection distance is fixed. Once the intensity of the emitter and the alignment of emitter and detector are determined, the sensor will have stable, repeatable performance under most lighting conditions.

The standard TSSP IR-receivers have a threshold irradiance of 0.4 mW/m^2 to 0.7 mW/m^2 which provides sufficient receiving range and ambient light robustness in most indoor applications. For outdoor use or applications where the receiver can be exposed to direct sunlight, we recommend to use a receiver with reduced sensitivity like the TSSP93038ZA. It is also possible to protect standard sensitivity TSSP receivers against direct sunlight by using an aperture or an attenuation filter in front of the receiver. For further support on TSSP sensor solutions, please contact: IRR@vishay.com.

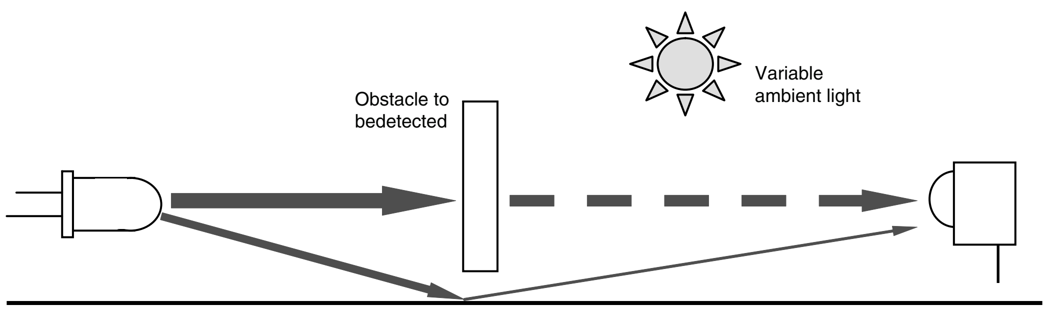
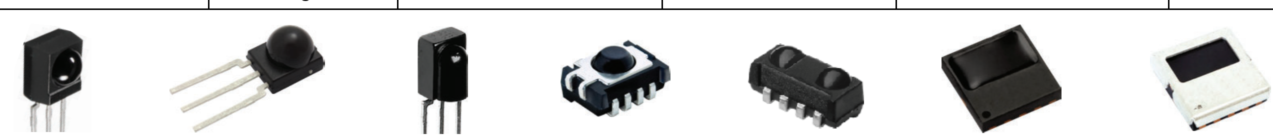


Fig. 6 - Stray Light in a Light Barrier Application can Produce a False Response

FIX GAIN RECEIVER FOR SENSOR APPLICATIONS					
PART NUMBER (1)	**PACKAGE**	**DIMENSION (mm)**	**VIEWING ANGLE (°)**	**IRRADIANCE (mW/m^2)**	**RANGE (m)**
TSSP94038	Mold	6.0 W x 6.95 H x 5.6 D	± 45	0.4	12
TSSP4038					
TSSP93038	Minimold	5.4 W x 6.35 H x 4.9 D	± 45	0.4	12
TSSP53038					
TSSP98038	Minicast	5.0 W x 6.95 H x 4.8 D	± 45	0.7	8
TSSP58038					
TSSP96038	Panhead	7.5 W x 5.3 H x 4.0 D	± 50	0.7	8
TSSP6038					
TSSP95038	Heimdall	6.8 W x 3.0 H x 3.2 D	± 50	0.7	8
TSSP77038					
TSSP57038	Belobog	3.95 W x 3.95 H x 0.8 D	± 75	0.7	8
TSSP57038H	Belobog shield	3.95 W x 3.95 H x 1.0 D			



Mold · Minimold · Minicast · Panhead · Haimdall · Belobog · Belobog shield

DETECTOR WITH PWM OUTPUT

The typical application of the TSSP4P38 is a reflective sensor with analog information contained in its output. Such a sensor is evaluating the time required by the AGC to suppress a quasi continuous signal. The time required to suppress such a signal is longer when the signal is strong than when the signal is weak, resulting in a pulse length corresponding to the distance of an object from the sensor.

This kind of analog information can be evaluated by a microcontroller. The absolute amount of reflected light depends much on the environment and is not evaluated. Only sudden changes of the amount of reflected light, and therefore changes in the pulse width, are evaluated using this application.

Example of a signal pattern:

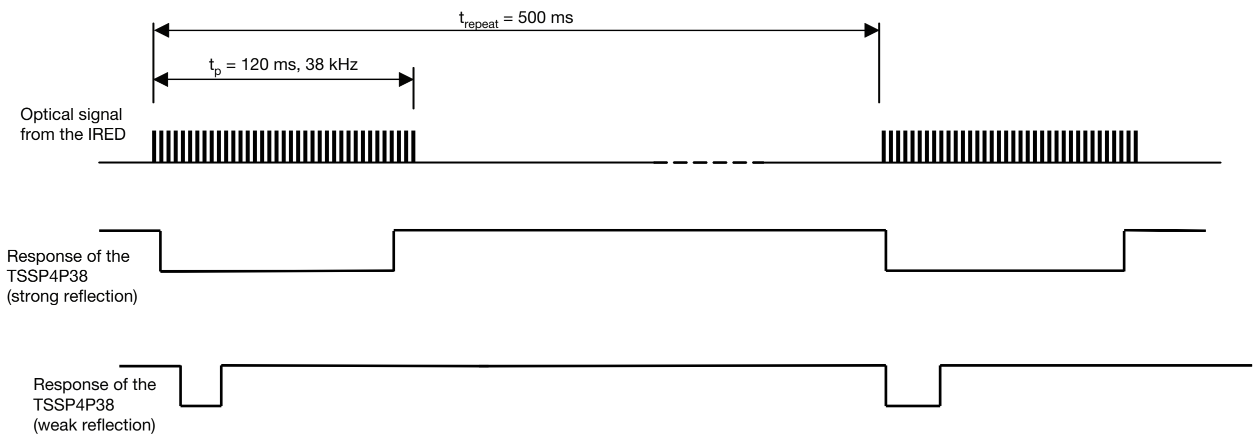


Fig. 7 - Protection Circuit Against Over Voltage Spikes

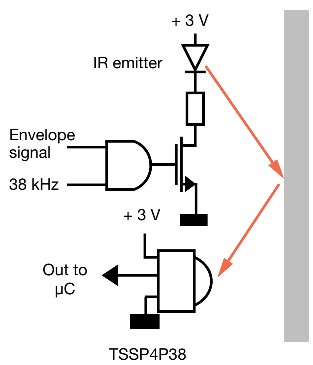
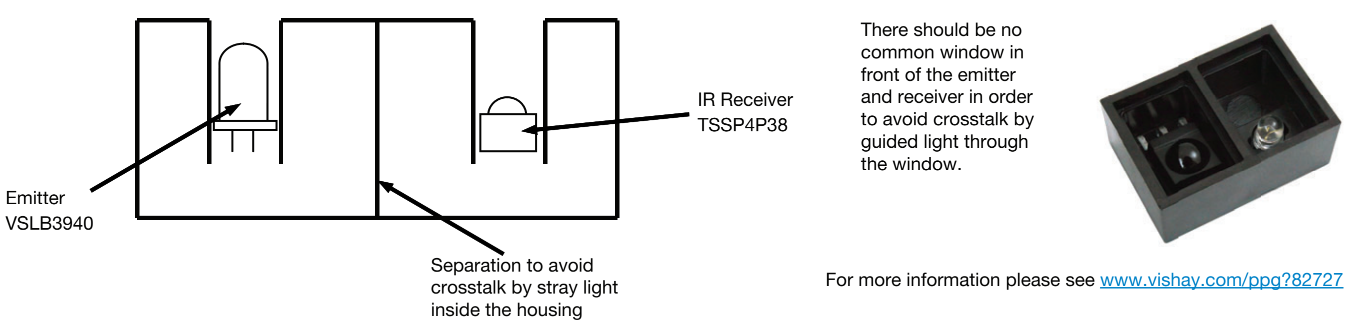


Fig. 8 - Proximity Sensing



For more information please see www.vishay.com/ppg?82727

Fig. 9 - Example of a Sensor Housing for a Reflective Sensor

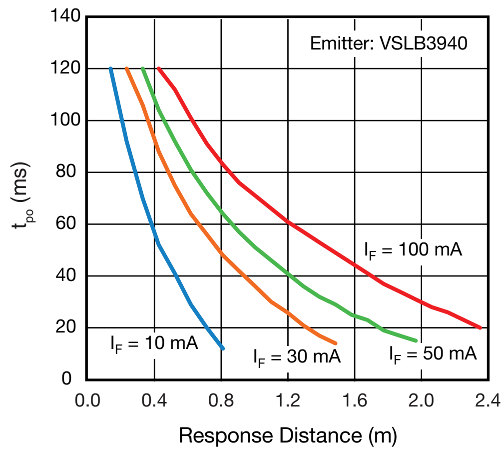


Fig. 10 - t_{po} vs. Distance Kodak Gray Card Plus 15 %

The logarithmic characteristic of the AGC in the TSSP4P38 results in an almost linear relationship between distance and pulse width. Ambient light has also some impact to the pulse width of this kind of sensor, making the pulse shorter.

APPLICATION IN BATTERY-POWERED SYSTEMS

There are two crictical paramters when using the IR receiver modules in battery-powerded systems: the supply voltage and the supply current. The best properties regarding both parameters have the IR receivers of the TSOP3xxxx family. These devices have low supply current of about 0.35 mA only and they can work at low supply voltages to provide a function even with almost empty batteries. The lowest specified supply voltage is 2.5 V, however typically it can operate even below 2 V.

If the supply current of the IR receiver modules is too high for continuous operation then a pulsed supply voltage can help to further save battery power. For the best response time, the duty cycle of the supply voltage should be selected such that the supply is pulsed once during the wake up signal of the IR command as shown in fig. 8 and 9. If the IR receiver senses a signal in this time window, then the supply voltage is turned on for a longer period of time to receive the full data command.

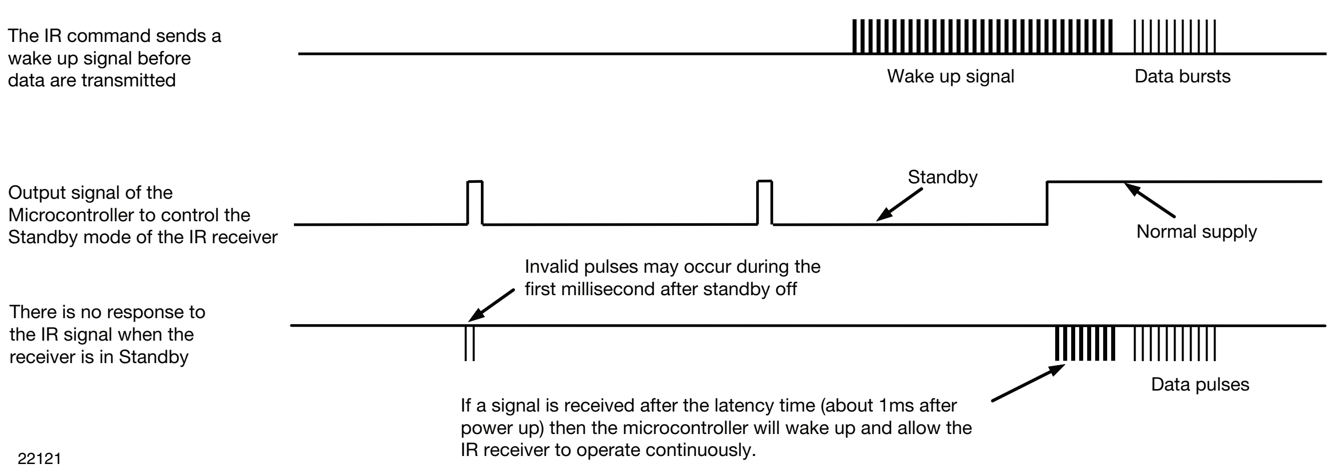


Fig. 11 - Example for a Battery-Saving Mode With TSOP3xxxx IR Receivers

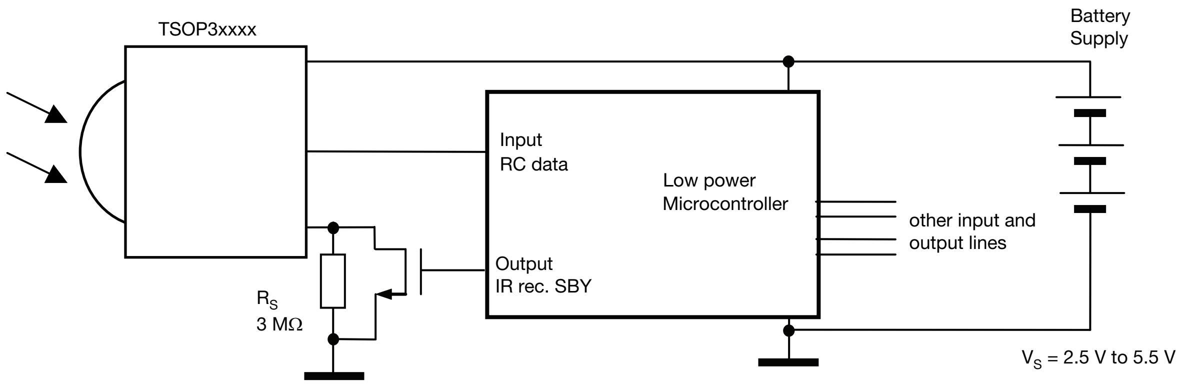


Fig. 12 - Circuit Proposal for Pulsing the Supply Voltage of the TSOP3xxxx

The actual stand-by supply current of the IR receiver when used in this application depends on the ratio of “on / off time”. In the case of a 2 ms on time and 200 ms off time, the stand-by supply current is about 3.5 μA for the IR receiver. This would allow a battery life of more than 2 years.

To achieve this performance, a pre-burst (wake up time) of 202 ms is needed in this example.

If the TSOP3xxxx is disconnected from supply voltage and reconnected again then the gain level is on a default value. It will last up to 100 ms until the gain has settled to an optimum level that is well adapted to the ambient noise level.

However, if the TSOP3xxxx is set to the standby mode (as shown in Fig. 12) then it can memorize the gain setting during the off period. Each time when it is powered up the gain is on the correct level. Hence there are no spurious pulses in bright ambient during the on period when the receiver is operated in this kind of power saving mode.

The standby mode of the TSOP3xxxx means that it is supplied through a high impedance serial resistor. In that case the circuit of the TSOP3xxxx is deactivated and the supply current becomes almost zero. However, the gain level of the AGC is still memorized.

The easiest way to activate the standby mode is to operate the TSOP3xxxx with a series resistor at about 2 MΩ in case of a 3 V supply voltage or 3 MΩ in case of a 5 V supply voltage.

APPLICATIONS WITH BI-DIRECTIONAL TRANSMISSION

A two-way communication in half duplex mode is possible with the Vishay IR receiver modules. Full duplex mode is not possible as the selectivity of the receivers using two IR channels (e.g. one at 30 kHz and one at 56 kHz) at the same time and in the same space is not sufficient.

In a bi-directional IR transmission, the receiver will usually see the transmitted signal of both sites, the signal that is sent from the other site as well as the signal that is sent from the receiver site. In such an application, the transmitted signal is usually much stronger than the received signal. In order to allow a fast turn around time between sending and receiving we recommend the TSOP9xxxx or TSOP1xxxx series receivers. At these receivers an idle period of 0.5 ms for TSOP9xxxx series and 1 ms for TSOP1xxxx series is sufficient.