

Standard SMD LED PLCC-2



DESCRIPTION

This device has been designed to meet the increasing demand for white SMD LED.

The package of the VLMW41.. is the PLCC-2.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled with a mixture of epoxy and TAG phosphor.

The TAG phosphor converts the blue emission partially to yellow, which mixes with the remaining blue to give white.

PRODUCT GROUP AND PACKAGE DATA

- Product group: LED
- Package: SMD PLCC-2
- · Product series: standard
- Angle of half intensity: ± 60°

FEATURES

- High efficient InGaN technology
- Chromaticity coordinate categorized according to CIE1931 per packing unit
- Typical color temperature 5000 K
- · EIA and ICE standard package
- · Compatible with reflow, vapor phase and wave RoHS solder processes according to CECC 00802 and J-STD-020
- Available in 8 mm tape reel
- Preconditioning according to JEDEC[®] level 2a
- ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- Luminous intensity ratio in one packing unit $I_{Vmax}/I_{Vmin} \le 1.6$
- AEC-Q101 qualified
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Camera flash light
- · Signal and symbol luminaire
- Marker lights
- · Interior and exterior automotive lighting: brake lights, turn lights, backlighting, side markers
- Indicator lighting

PARTS TABLE														
PART	COLOR	LUMINOUS INTENSITY (mcd)		at I _F (mA)	COORDINATE (x, y)		at I _F (mA)	FORWARD VOLTAGE (V)		at I _F (mA)	TECHNOLOGY			
		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		
VLMW41R1T1-7K8L-08	White	112	275	355	10	-	0.345, 0.352	-	10	-	3.3	4.2	20	InGaN / TAG on Sapphire

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified) VLMW41						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
DC forward current	$T_{amb} \le 80 \ ^{\circ}C$	١ _F	20	mA		
Surge forward current	$t_p \le 10 \ \mu s$	I _{FSM}	0.1	A		
Power dissipation		Pv	84	mW		
Junction temperature		Tj	110	°C		
Storage temperature range		T _{stg}	-40 to +100	°C		
Operating temperature range		T _{amb}	-40 to +100	°C		
Thermal resistance junction / ambient	Mounted on PC board (pad size > 16 mm ²)	R _{thJA}	360	K/W		

Rev. 1.3, 22-Mar-16

For technical questions, contact: LED@vishay.com

Document Number: 81190





OPTICAL AND ELECTRICAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified) **VLMW41.., WHITE**

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity	I _F = 10 mA	VLMW41R1T1	IV	112	275	355	mcd
Chromaticity coordinate x acc. to CIE 1931	I _F = 10 mA		х	-	0.345	-	
Chromaticity coordinate y acc. to CIE 1931	I _F = 10 mA		у	-	0.352	-	
Angle of half intensity	I _F = 10 mA		φ	-	± 60	-	deg
Forward voltage	I _F = 20 mA		V _F	-	3.3	4.2	V
Temperature coefficient of V _F	I _F = 10 mA		TC _{VF}	-	-3	-	mV/K
Temperature coefficient of Iv	I _F = 10 mA		TCIV	-	-0.4	-	%/K

Note

Not designed for reverse operation

LUMINOUS INTENSITY CLASSIFICATION						
GROUP	LIGHT INTENSITY (mcd)					
STANDARD	OPTIONAL	MIN.	MAX.			
R	1	112	140			
	2	140	180			
S	1	180	224			
	2	224	280			
Т	1	280	355			

Note

 Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of ± 11 %. The above type numbers represent the order groups which

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel). In order to ensure availability, single brightness groups are not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.

In order to ensure availability, single wavelength groups are not be orderable.

CROSSING TABLE					
VISHAY	OSRAM				
VLMW41	LWT67C				

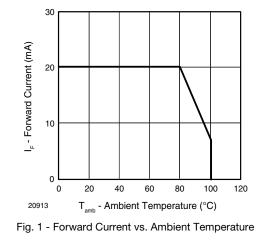
TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

CHROMATICITY COORDINATED GROUPS FOR WHITE SMD LED

	X	Y			
	0.330	0.330			
7L	0.330	0.347			
76	0.347	0.371			
	0.345	0.352			
	0.330	0.310			
7K	0.330	0.330			
	0.338	0.342			
	0.352	0.344			
	0.345	0.352			
8L	0.347	0.371			
0L	0.367	0.401			
	0.364	0.380			
	0.352	0.344			
8K	0.338	0.342			
UIX	0.364	0.380			
	0.360	0.357			

Note

• Chromaticity coordinate groups are tested at a current pulse duration of 25 ms and a tolerance of ± 0.01.



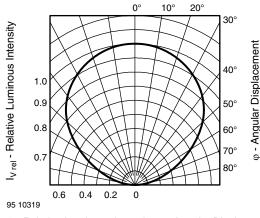


Fig. 2 - Relative Luminous Intensity vs. Angular Displacement

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000



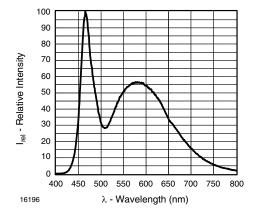


Fig. 3 - Relative Intensity vs. Wavelength

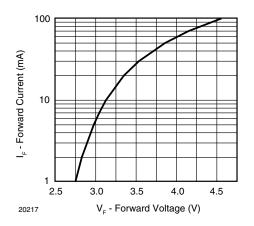


Fig. 4 - Forward Current vs. Forward Voltage

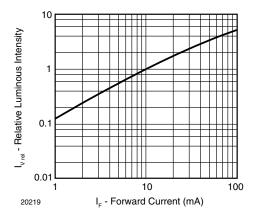


Fig. 5 - Relative Luminous Intensity vs. Forward Current

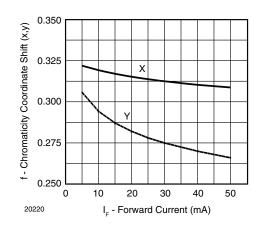


Fig. 6 - Chromaticity Coordinate Shift vs. Forward Current

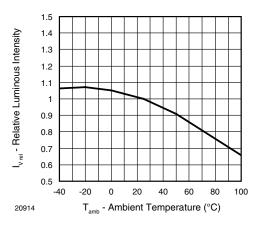


Fig. 7 - Relative Luminous Intensity vs. Ambient Temperature

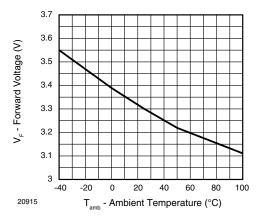


Fig. 8 - Forward Voltage vs. Ambient Temperature



VLMW41R1T1-7K8L

Vishay Semiconductors

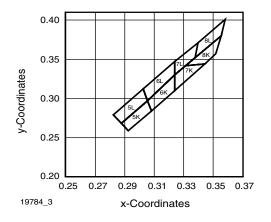
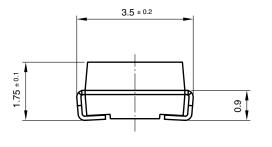
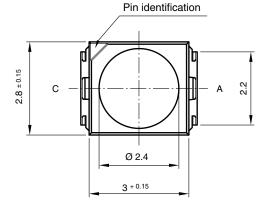


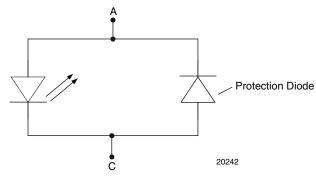
Fig. 9 - Coordinates of Colorgroups











Rev. 1.3, 22-Mar-16

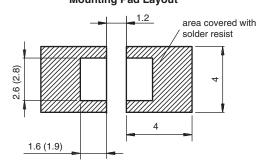
4 For technical questions, contact: <u>LED@vishay.com</u> Document Number: 81190

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

Mounting Pad Layout

technical drawings according to DIN

specifications

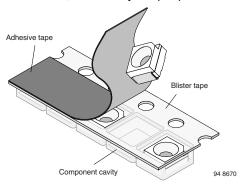




METHOD OF TAPING / POLARITY AND TAPE AND REEL

SMD LED (VLM.3..., VLM.4... - SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



TAPING OF VLM.3..., VLM.4...

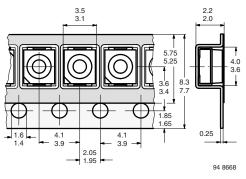


Fig. 10 - Tape Dimensions in mm for PLCC-2

REEL PACKAGE DIMENSIONS IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

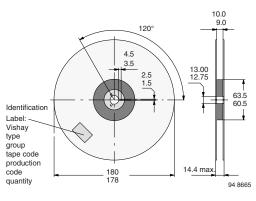
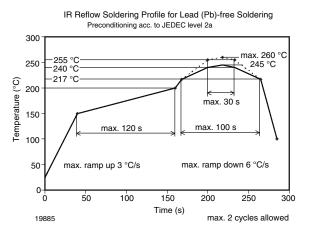


Fig. 11 - Reel Dimensions - GS08

Rev. 1.3, 22-Mar-16

5



SOLDERING PROFILE

Fig. 12 - Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020)

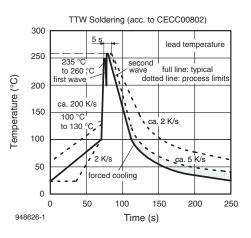
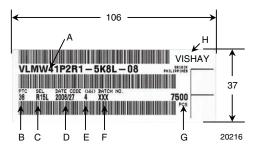


Fig. 13 - Double Wave Soldering of Opto Devices (all Packages)



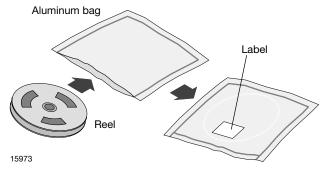
BAR CODE PRODUCT LABEL (example)



- A) Type of component
- B) Manufacturing plant
- C) SEL selection code (bin): e.g.: R1 = code for luminous intensity group 5L = code for chrom. coordinate group
- D) Date code year / week
- E) Day code (e.g. 4: Thursday)
- F) Batch no.
- G) Total quantity
- H) Company code

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

Vishay Semiconductors

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

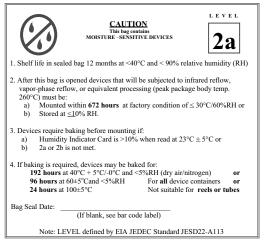
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 $^{\circ}\text{C}$ + 5 $^{\circ}\text{C}$ and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABEL

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

6



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2024 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED

Revision: 01-Jan-2024