VEMI85AC-HGK

Vishay Semiconductors

8-Channel EMI-Filter with ESD-Protection

FEATURES

- Ultra compact LLP3313-17L package
- Low package profile of 0.6 mm
- 8-channel EMI-filter
- Low leakage current
- Line resistance $R_S = 100 \Omega$
- Typical cut off frequency f_{3dB} = 240 MHz
- ESD-protection acc. IEC 61000-4-2
 - ± 10 kV contact discharge ± 12 kV air discharge
- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

MARKING (example only)

21840

16 15 14 13 12 11 10 9

20388



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Y = type code (see table below) XX = date code

DESIGN SUPPORT TOOLS



ORDERING INFORMATION					
DEVICE NAME	ORDERING CODE	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY		
VEMI85AC-HGK	VEMI85AC-HGK-GS08	3000	15 000		

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PACKAGE DATA							
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS	
VEMI85AC-HGK	LLP3313-17L	9W	7.4 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	R TEST CONDITIONS		VALUE	UNIT	
Peak pulse current	All I/O pin to pin 17; acc. IEC 61000-4-5; $t_p = 8/20 \ \mu$ s; single shot	I _{PPM}	4	А	
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 10	kV	
	Air discharge acc. IEC 61000-4-2; 10 pulses	V _{ESD}	± 12		
Operating temperature	Junction temperature	ТJ	-40 to +125	°C	
Storage temperature		T _{STG}	-55 to +150	°C	

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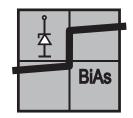


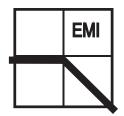
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APPLICATION NOTE

With the VEMI85AC-HGK 8 different signal or data lines can be filtered and clamped to ground. Due to the different clamping levels in forward and reverse direction the clamping behaviour is <u>Bi</u>directional and <u>Asymmetric</u> (BiAs).

L1 _{OUT}
L2 _{OUT}
L3 _{OUT}
L4 _{OUT}
L5 _{OUT}
L6 _{OUT}
L7 _{OUT}
L8 _{OUT}





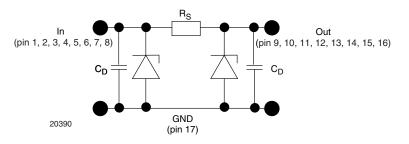
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The 8 independent EMI-filter are placed between

pin 1 and pin 16, pin 2 and pin 15, pin 3 and pin 14, pin 4 and pin 13, pin 5 and pin 12, pin 6 and pin 11, pin 7 and pin 10 and pin 8 and pin 9.

They all are connected to a common ground pin 17 on the backside of the package.

The circuit diagram of one EMI-filter-channel shows two identical Z-diodes at the input to ground and the output to ground. These Z-diodes are characterized by the breakthrough voltage level (V_{BR}) and the diode capacitance (C_D). Below the breakthrough voltage level the Z-diodes can be considered as capacitors. Together with these capacitors and the line resistance R_S between input and output the device works as a low pass filter. Low frequency signals (f < f_{3dB}) pass the filter while high frequency signals (f > f_{3dB}) will be shorted to ground through the diode capacitances C_D .



Each filter is symmetrical so that both ports can be used as input or output.

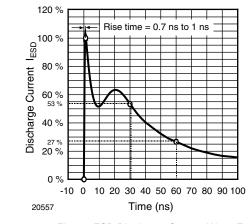
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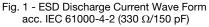


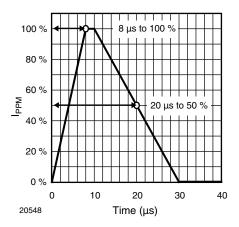
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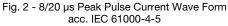
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of channels which can be protected	N _{channel}	-	-	8	channe
Reverse stand off voltage	Max. reverse working voltage	V _{RWM}	-	-	5	V
Reverse voltage	at I _R = 1 μA	V _R	5	-	-	V
Reverse current	at $V_R = V_{RWM}$	I _R	-	< 0.1	1	μA
Reverse break down voltage	at I _R = 1 mA	V _{BR}	6	6.8	-	V
Pos. clamping voltage	at $I_{PP} = 1$ A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}	-	-	7	V
	at $I_{PP} = I_{PPM} = 2$ A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}	-	-	8	V
Neg. clamping voltage	at I_{PP} = -1 A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}	-1.4	-	-	V
	at $I_{PP} = I_{PPM} = -2$ A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}	-1.6	-	-	V
Input capacitance	at $V_R = 0 V$; f = 1 MHz	C _{IN}	-	20	-	pF
	at V _R = 2.5 V; f = 1 MHz	C _{IN}	-	13	-	pF
ESD-clamping voltage	at ± 10 kV ESD-pulse acc. IEC 61000-4-2	V _{CESD}	-	7.5	-	V
Line resistance	Measured between input and output; $I_S = 10 \text{ mA}$	R _S	90	100	110	Ω
Cut-off frequency	V_{IN} = 0 V; measured in a 50 Ω system	f _{3dB}	-	240	-	MHz

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)



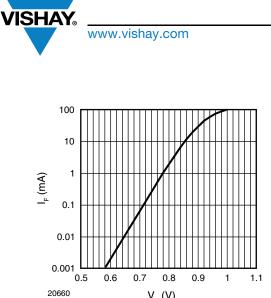








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 20660 $$V_{\rm F}$$ (V) Fig. 3 - Typical Forward Current I_F vs. Forward Voltage $V_{\rm F}$

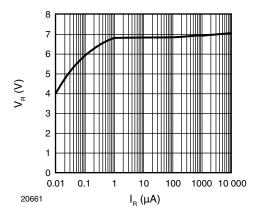


Fig. 4 - Typical Reverse Voltage V_R vs. Reverse Current I_R

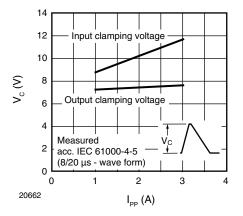


Fig. 5 - Typical Peak Clamping Voltage V_C vs. Peak Pulse Current I_{PP}

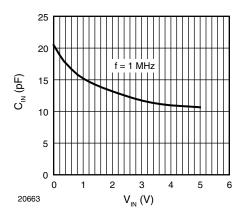


Fig. 6 - Typical Input Capacitance C_{IN} vs. Input Voltage V_{IN}

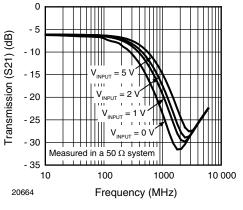


Fig. 7 - Typical Small Signal Transmission (S21) at Z_O = 50 Ω

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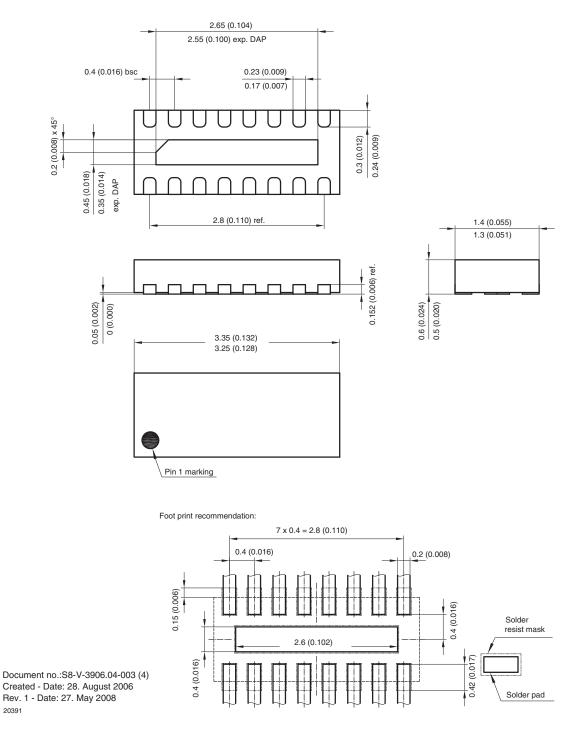
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PACKAGE DIMENSIONS in millimeters (inches): LLP3313-17L



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