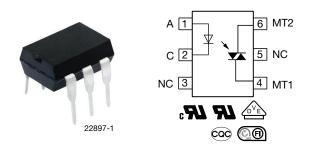


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K3010P, K3012P

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Optocoupler, Phototriac Output, Non-Zero Crossing, 250 VDRM



LINKS TO ADDITIONAL RESOURCES



DESCRIPTION

The K301xP series consists of a phototriac optically coupled to a gallium arsenide infrared-emitting diode in a 6-lead plastic dual inline package.

The non-zero crossing functionality enables full wave control. Featuring galvanic and electrical noise isolation, the output is able to directly switch AC loads or drive medium to high power TRIACs.

FEATURES

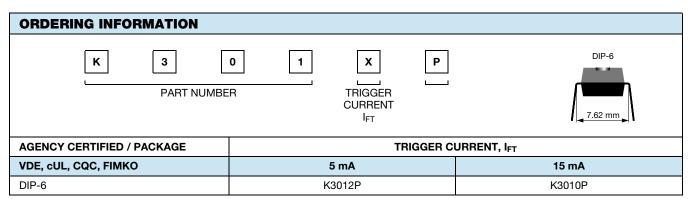
- 250 V blocking voltage
- Wide range of trigger current
- 100 mA_{RMS} on-state current
- Wide temperature range -55 °C to +100 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Power TRIAC driver
- Isolated AC load switch
- Air condition
- Heaters
- White goods
- Industrial controls
- Office equipment

AGENCY APPROVALS

- <u>UL</u>
- <u>cUL</u>
- DIN EN 60747-5-5 (VDE 0884-5)
- CQC: GB4943-1-2011
- <u>CQC: GB8898-2011</u>
- <u>FIMKO</u>



Note

• Additional options may be possible, please contact sales office



RoHS

COMPLIANT



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
Reverse voltage		V _R	5	V		
Forward current		IF	80	mA		
Forward surge current	t _p ≤ 10 μs	I _{FSM}	3	A		
Power dissipation		P _{diss}	100	mW		
Junction temperature		Тj	125	°C		
OUTPUT						
Off state output terminal voltage		V _{DRM}	250	V		
On state RMS current		I _{TRM}	100	mA		
Peak surge current, non-repetitive	$t_p \le 10 \text{ ms}$	I _{TMS}	1.5	А		
Power dissipation		P _{diss}	300	mW		
Junction temperature		Тj	125	°C		
COUPLER						
Total power dissipation		P _{tot}	350	mW		
Storage temperature range		T _{stg}	-55 to +150	°C		
Ambient temperature range		T _{amb}	-55 to +100	°C		
Soldering temperature	2 mm from case, t \leq 10 s	T _{sld}	260	°C		

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25 \degree C$, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT	INPUT						
Forward voltage	I _F = 50 mA		V _F	-	1.25	1.6	V
Junction capacitance	$V_R = 0, f = 1 MHz$		Cj	-	50	-	pF
OUTPUT							
Forward peak off-state voltage (repetitive)	I _{RDM} = 100 nA		V _{DRM} ⁽¹⁾	250	-	-	V
Peak on-state voltage	I _{TM} = 100 mA		V _{TM}	-	1.5	3	V
Critical rate of rise of off-state voltage	l _{FT} = 0, l _{FT} = 30 mA		dV/dt _{cr}	-	10	-	V/µs
			dV/dt _{crq}	0.1	0.2	-	V/µs
COUPLER ⁽²⁾							
Collector emitter trigger current	V_{S} = 3 V, R_{L} = 150 Ω	K3010P	I _{FT}	-	8	15	mA
		K3012P	I _{FT}	-	2	5	mA
Holding current	$I_F = 10 \text{ mA}, V_S \ge 3 \text{ V}$		Ι _Η	-	100	-	μA

Notes

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements

⁽¹⁾ Test voltage must be applied within dV/dt ratings

⁽²⁾ I_{FT} is defined as a minimum trigger current

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Test condition: dV/dt_{cr} $V_S = 2/3 V_{DRM}$ (sine wave) $R_1 = 33 k\Omega$

dV/dt crq

 $V_{eff.} = 30 V$

(sine wave) $R_L = 2 k\Omega$

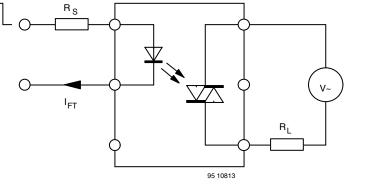
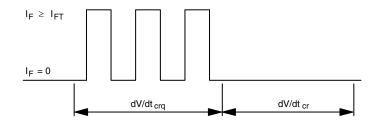


Fig. 1 - Test Circuit for dV/dt_{cr} and dV/dt_{crq}



dV/dt cr

Highest value of the "rate of rise of off-state voltage" which does not cause any switching from the off state to the on state

dV/dt _{crq} 95 10814 Highest value of the "rate of rise of communicating voltage" which does not switch on the device again, after the voltage has decreased to zero and the trigger current is switched from I_{FT} to zero

Fig. 2

SAFETY AND INSULATION RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Climatic classification	According to IEC 68 part 1		55 / 100 / 21		
Pollution degree	According to DIN VDE 0109		2		
Comparative tracking index	Insulation group IIIa	CTI	175		
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V _{ISO}	4420	V _{RMS}	
Tested withstanding isolation voltage	According to UL1577, t = 1 s	V _{ISO}	5300	V _{RMS}	
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V _{IOTM}	8000	V _{peak}	
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	VIORM	890	V _{peak}	
	$T_{amb} = 25 \ ^{\circ}C, \ V_{IO} = 500 \ V$	R _{IO}	≥ 10 ¹²	Ω	
Isolation resistance	$T_{amb} = 100 \ ^{\circ}C, \ V_{IO} = 500 \ V$	R _{IO}	≥ 10 ¹¹	Ω	
Output safety power		P _{SO}	265	mW	
Input safety current		I _{SI}	130	mA	
Input safety temperature		T _S	150	°C	
Creepage distance			≥7	mm	
Clearance distance	DIP-6		≥7	mm	
Insulation thickness		DTI	≥ 0.4	mm	
Input to output test voltage, method A	V_{IORM} x 1.6 = V_{PR} , 100 % sample test with t_M = 10 s, partial discharge < 5 pC	V _{PR}	1424	V _{peak}	

Note

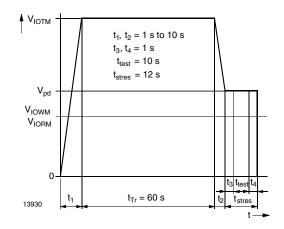
 According to DIN EN60747-5-5 (see figure 4). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits

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300 P_{si} (mW) 250 Max. Safety Ratings 200 150 (mA) 100 50 0 0 25 50 75 100 125 150 Tamb - Ambient Temperature (°C)

Fig. 3 - Safety Parameter Derating Diagram



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Fig. 4 - Test Pulse Diagram for Sample Test according to DIN EN60747-5-5 / DIN EN60747-; IEC 60747



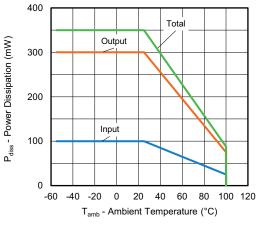


Fig. 5 - Total Power Dissipation vs. Ambient Temperature

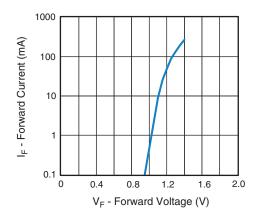


Fig. 6 - Forward Current vs. Forward Voltage

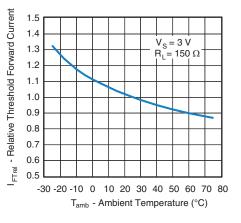


Fig. 7 - Relative Threshold Forward Current vs. Ambient Temperature

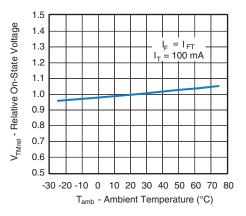


Fig. 8 - Relative On-State vs. Ambient Temperature

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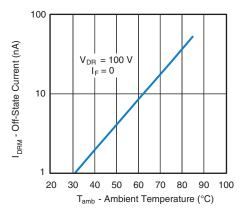
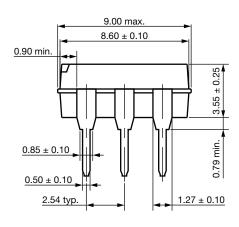
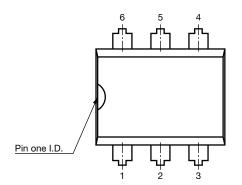


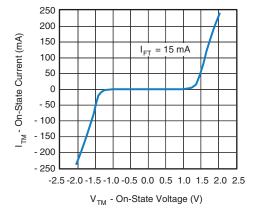
Fig. 9 - Off-State Current vs. Ambient Temperature

PACKAGE DIMENSIONS (in millimeters)

DIP-6



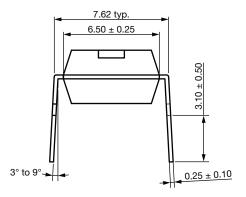




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Fig. 10 - On-State Current vs. On-State Voltage



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PACKAGE MARKING

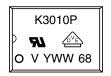


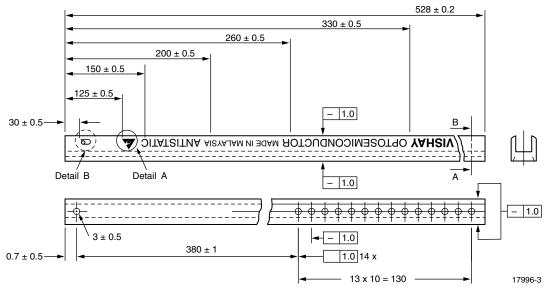
Fig. 11 - Example of K3010P

Notes

- "YWW" is the date code marking (Y = year code, WW = week code)
- The VDE logo is only marked on option1 parts

PACKING INFORMATION (in millimeters)

Tube





DEVICES PER TUBES					
ТҮРЕ	UNITS/TUBE	TUBES/BOX	UNITS/BOX		
DIP-6	50	40	2000		

DIP-6

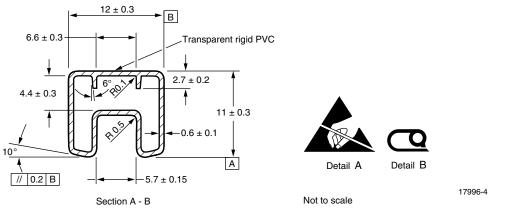


Fig. 13 - Tube Shipping Medium

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SOLDER PROFILES

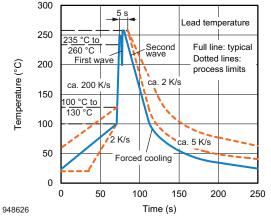


Fig. 14 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices

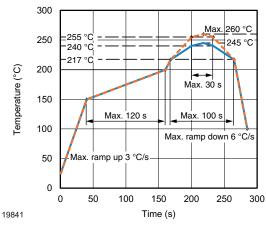
HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2

Floor life: unlimited

Conditions: $T_{amb} < 30\ ^\circ C,\ RH < 85\ \%$

Moisture sensitivity level 1, according to J-STD-020







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