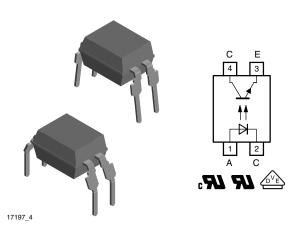


# Optocoupler, Phototransistor Output, High Temperature, 110 °C, Rated



#### **LINKS TO ADDITIONAL RESOURCES**



#### **DESCRIPTION**

The TCET111., TCET111.G consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4 pin plastic dual inline package.

#### **APPLICATIONS**

Circuits for safe protective separation against electrical shock according to safety class II (reinforced isolation):

- for appl. class I to IV at mains voltage ≤ 300 V
- for appl. class I to III at mains voltage ≤ 600 V according to DIN EN 60747-5-5 (VDE 0884), suitable for:
  - Switch-mode power supplies
  - Line receiver
  - Computer peripheral interface
  - Microprocessor system interface

#### **FEATURES**

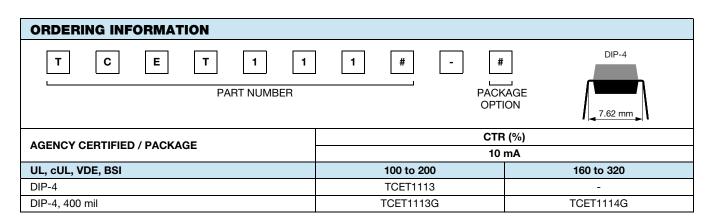
- · CTR offered in 9 groups
- · Isolation materials according to UL 94 V-0
- Pollution degree 2 (DIN / VDE 0110 / resp. IEC 60664)
- Climatic classification 55 / 100 / 21 (IEC 60068 part 1)



- Special construction: therefore, extra low coupling capacity of typical 0.2 pF, high common mode rejection
- Low temperature coefficient of CTR
- Temperature range -40 °C to +110 °C
- Rated impulse voltage (transient overvoltage)  $V_{IOTM} = 6 \text{ kV}_{peak}$
- Isolation test voltage (partial discharge test voltage)  $V_{pd} = 1.6 \text{ kV}$
- Rated isolation voltage (RMS includes DC)
   V<sub>IOWM</sub> = 600 V<sub>RMS</sub>
- Rated recurring peak voltage (repetitive) V<sub>IORM</sub> = 850 V<sub>peak</sub>
- Creepage current resistance according to VDE 0303/ IEC 60112 comparative tracking index: CTI ≥ 175
- Thickness through insulation ≥ 4 mm
- External creepage distance > 8 mm
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **AGENCY APPROVALS**

- <u>UL</u>
- cUL
- DIN EN 60747-5-5 (VDE 0884)
- BSI: EN 62368-1:2014



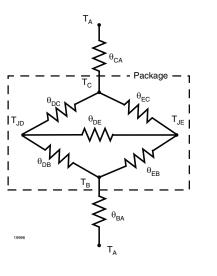


<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
Reverse voltage		$V_R$	6	V		
Forward current		I <sub>F</sub>	60	mA		
Forward surge current	$t_p \le 10 \; \mu s$	I <sub>FSM</sub>	1.5	Α		
OUTPUT						
Collector emitter voltage		V <sub>CEO</sub>	70	V		
Emitter collector voltage		V <sub>ECO</sub>	7	V		
Collector current		I <sub>C</sub>	50	mA		
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA		
COUPLER						
Isolation test voltage (RMS)	t = 1 s	V <sub>ISO</sub>	5000	V <sub>RMS</sub>		
Operating ambient temperature range		T <sub>amb</sub>	-40 to +110	°C		
Storage temperature range		T <sub>stg</sub>	-55 to +125	°C		
Soldering temperature (1)	2 mm from case, $\leq$ 10 s	T <sub>sld</sub>	260	°C		

#### **Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
  implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
  maximum ratings for extended periods of the time can adversely affect reliability
- (1) Refer to wave profile for soldering conditions for through hole devices (DIP)

THERMAL CHARACTERISTICS				
PARAMETER	SYMBOL	VALUE	UNIT	
LED power dissipation	P <sub>diss</sub>	70	mW	
Output power dissipation	P <sub>diss</sub>	70	mW	
Maximum LED junction temperature	T <sub>jmax.</sub>	125	°C	
Maximum output die junction temperature	T <sub>jmax.</sub>	125	°C	
Thermal resistance, junction emitter to board	$\theta_{\sf EB}$	173	°C/W	I HDC I
Thermal resistance, junction emitter to case	$\theta_{\sf EC}$	149	°C/W	
Thermal resistance, junction detector to board	$\theta_{DB}$	111	°C/W	$\theta_{DB}$
Thermal resistance, junction detector to case	$\theta_{DC}$	127	°C/W	<u> </u>
Thermal resistance, junction emitter to junction detector	$\theta_{ED}$	173	°C/W	
Thermal resistance, board to ambient (1)	$\theta_{BA}$	197	°C/W	19996
Thermal resistance, case to ambient (1)	$\theta_{\sf CA}$	4041	°C/W	



#### Notes

- The thermal model is represented in the thermal network below. Each resistance value given in this model can be used to calculate the
  temperatures at each node for a given operating condition. The thermal resistance from board to ambient will be dependent on the type of
  PCB, layout and thickness of copper traces. For a detailed explanation of the thermal model, please reference Vishay's "Thermal
  Characteristics of Optocouplers" application note
- (1) For 2 layer FR4 board (4" x 3" x 0.062")



<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT			
INPUT	INPUT								
Forward voltage	$I_F = 50 \text{ mA}$	$V_{F}$	-	1.25	1.6	V			
Junction capacitance	$V_R = 0$ , $f = 1$ MHz	C <sub>j</sub>	-	50	-	pF			
OUTPUT									
Collector emitter voltage	$I_C = 1 \text{ mA}$	$V_{CEO}$	70	-	-	V			
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7	-	-	V			
Collector emitter cut-off current	$V_{CE} = 20 \text{ V}, I_F = 0 \text{ A}$	I <sub>CEO</sub>	-	10	100	nA			
COUPLER	COUPLER								
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 1 \text{ mA}$	V <sub>CEsat</sub>	-	-	0.3	V			
Cut-off frequency	$V_{CE}$ = 5 V, $I_F$ = 10 mA, $R_L$ = 100 $\Omega$	f <sub>c</sub>	-	110	-	kHz			
Coupling capacitance	f = 1 MHz	C <sub>k</sub>	-	0.6	-	pF			

#### Note

<sup>(1)</sup> Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>		TCET1112G	CTR	22	45	-	%
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 1 mA	TCET1113, TCET1113G	CTR	34	70	-	%
		TCET1114G	CTR	56	90	-	%
	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	TCET1110G	CTR	50	-	600	%
		TCET1112G	CTR	63	-	125	%
	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 10 mA	TCET1113, TCET1113G	CTR	100	-	200	%
		TCET1114G	CTR	160	-	320	%

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Partial discharge test voltage - routine test	100 %, t <sub>test</sub> = 1 s	V <sub>pd</sub>	1.6			kV
Partial discharge test voltage -	$t_{Tr} = 60 \text{ s, } t_{test} = 10 \text{ s,}$ (see figure 2)	$V_{IOTM}$	8			kV
lot test (sample test)		$V_{pd}$	1.36			kV
Insulation resistance	V <sub>IO</sub> = 500 V	R <sub>IO</sub>	10 <sup>12</sup>			Ω
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	10 <sup>11</sup>			Ω
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 150 °C (construction test only)	R <sub>IO</sub>	10 <sup>9</sup>			Ω
Forward current		I <sub>si</sub>			130	mA
Power dissipation		P <sub>so</sub>			265	mW
Rated impulse voltage		$V_{IOTM}$			6	kV
Safety temperature		T <sub>si</sub>			150	°C

#### Note

According to DIN EN 60747-5-5 (see Fig. 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance
with the safety ratings shall be ensured by means of suitable protective circuits.

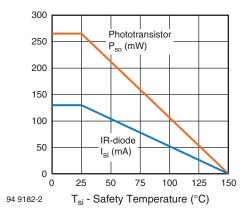


Fig. 1 - Derating Diagram

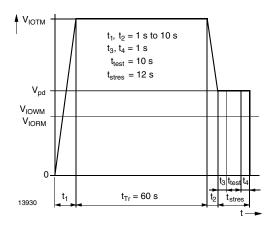


Fig. 2 - Test Pulse Diagram for Sample Test according to DIN EN 60747-5-5 (VDE 0884-5)

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Delay time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 3)	t <sub>d</sub>		3		μs	
Rise time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 3)	t <sub>r</sub>		3		μs	
Fall time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 3)	t <sub>f</sub>		4.7		μs	
Storage time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 3)	t <sub>s</sub>		0.3		μs	
Turn-on time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 3)	t <sub>on</sub>		6		μs	
Turn-off time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 3)	t <sub>off</sub>		5		μs	
Turn-on time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega,$ (see figure 4)	t <sub>on</sub>		9		μs	
Turn-off time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega,$ (see figure 4)	t <sub>off</sub>		10		μs	

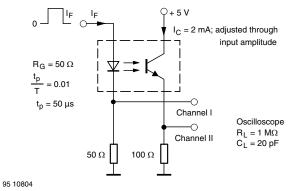


Fig. 3 - Test Circuit, Non-Saturated Operation

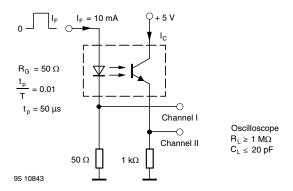


Fig. 4 - Test Circuit, Saturated Operation



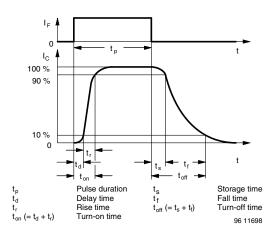


Fig. 5 - Switching Times

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

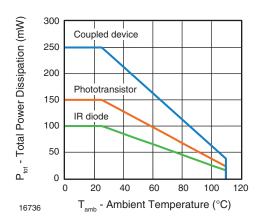


Fig. 6 - Total Power Dissipation vs. Ambient Temperature

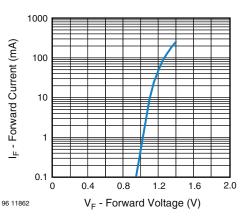


Fig. 7 - Forward Current vs. Forward Voltage

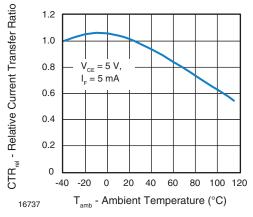


Fig. 8 - Relative Current Transfer Ratio vs.
Ambient Temperature

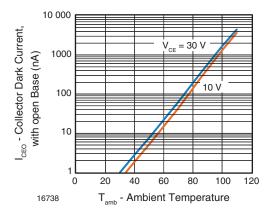


Fig. 9 - Collector Dark Current vs. Ambient Temperature



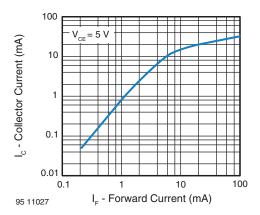


Fig. 10 - Collector Current vs. Forward Current

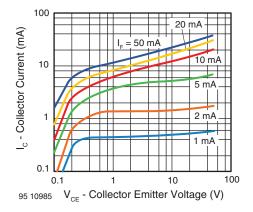


Fig. 11 - Collector Current vs. Collector Emitter Voltage

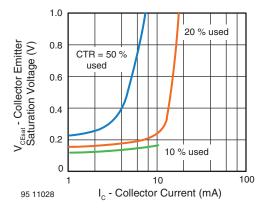


Fig. 12 - Collector Emitter Saturation Voltage vs. Collector Current

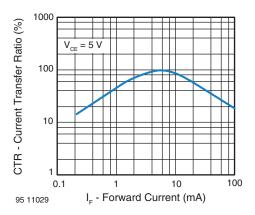


Fig. 13 - Current Transfer Ratio vs. Forward Current

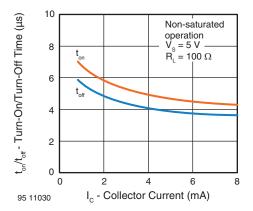


Fig. 14 - Turn-on/off Time vs. Collector Current

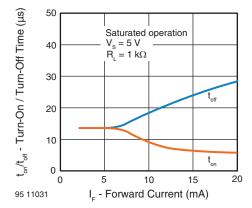
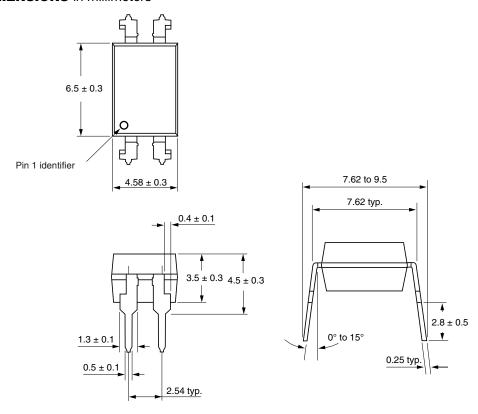
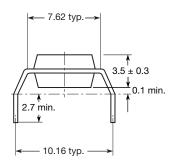


Fig. 15 - Turn-on/off Time vs. Forward Current

#### **PACKAGE DIMENSIONS** in millimeters

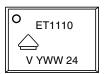


#### TCET1110G type



i178027-19

### **PACKAGE MARKING** (example)





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