IRF634

Vishay Siliconix



TO-220AB

PRODUCT SUMMARY

V_{DS} (V)

R_{DS(on)} (Ω)

Q_{gs} (nC)

Q_{gd} (nC)

Q_a max. (nC)

Configuration

Power MOSFET

S

N-Channel MOSFET

0.45

250

41

6.5

22

Single

 $V_{GS} = 10 V$

FEATURES

- Dynamic dV/dt rating
- Repetitive avalanche rated
- Fast switching
- Ease of paralleling
- Simple drive requirements



Note

This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

DESCRIPTION

Third generation power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free	IRF634PbF

ABSOLUTE MAXIMUM RATINGS (T _C	,			1 15417		
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-source voltage			V _{DS}	250	V	
Gate-source voltage			V _{GS}	± 20		
Continuous drain current	V _{GS} at 10 V	T _C = 25 °C T _C = 100 °C	1-	8.1		
		T _C = 100 °C	ID	5.1	A	
Pulsed drain current ^a			I _{DM}	32]	
Linear derating factor				0.59	W/°C	
Single pulse avalanche energy ^b			E _{AS}	300	mJ	
Repetitive avalanche current ^a			I _{AR}	8.1	А	
Repetitive avalanche energy ^a			E _{AR}	7.4	mJ	
Maximum power dissipation	T _C = 25 °C		PD	74	W	
Peak diode recovery dV/dt ^c			dV/dt	4.8	V/ns	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	*0		
Soldering recommendations (peak temperature) ^d	For	10 s		300	°C	
	6-32 or M3 screw			10	lbf ∙ in	
Mounting torque				1.1	N ⋅ m	

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)

- b. V_{DD} = 50 V, starting T_J = 25 °C, L = 7.3 mH, R_g = 25 Ω , I_{AS} = 8.1 A (see fig. 12)
- c. $I_{SD} \le 8.1$ A, dI/dt ≤ 120 A/µs, $V_{DD} \le V_{DS}$, $T_J \le 150$ °C

d. 1.6 mm from case

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1 For technical questions, contact: <u>hvm@vishay.com</u>



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THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	TYP.	MAX.	UNIT	
Maximum junction-to-ambient	R _{thJA}	-	62		
Case-to-sink, flat, greased surface	R _{thCS}	0.50	-	°C/W	
Maximum junction-to-case (drain)	R _{thJC}	-	1.7		

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static					•	•	
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA		250	-	-	V
V _{DS} temperature coefficient	$\Delta V_{DS}/T_J$	Reference	Reference to 25 °C, $I_D = 1 \text{ mA}$		0.37	-	V/°C
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		2.0	-	4.0	V
Gate-source leakage	I _{GSS}	$V_{GS} = \pm 20 V$		-	-	± 100	nA
		V _{DS} = 250 V, V _{GS} = 0 V		-	-	25	μA
Zero gate voltage drain current	IDSS	V _{DS} = 200 V, V _{GS} = 0 V, T _J = 125 °C		-	-	250	
Drain-source on-state resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 5.1 A ^b	-	-	0.45	Ω
Forward transconductance	9 _{fs}	V _{DS} = 50 V, I _D = 5.1 A ^b		1.6	-	-	S
Dynamic		•				•	
Input capacitance	C _{iss}	$V_{GS} = 0 V,$ $V_{DS} = 25 V,$ f = 1.0 MHz, see fig. 5		-	770	-	pF
Output capacitance	C _{oss}			-	190	-	
Reverse transfer capacitance	C _{rss}			-	52	-	
Total gate charge	Qg	V _{GS} = 10 V	$I_D = 5.6 \text{ A}, V_{DS} = 200 \text{ V},$ see fig. 6 and 13 ^b	-	-	41	nC
Gate-source charge	Q _{gs}			-	-	6.5	
Gate-drain charge	Q _{gd}			-	-	22	
Turn-on delay time	t _{d(on)}			-	9.6	-	ns
Rise time	t _r	V _{DD} = 1	V _{DD} = 125 V, I _D = 5.6 A,		21	-	
Turn-off delay time	t _{d(off)}	$R_g = 12 \Omega$, $R_D = 22 \Omega$, see fig. 10 ^b		-	42	-	
Fall time	t _f			-	19	-	
Gate input resistance	Rg	f = 1 MHz, open drain		0.6	-	2.9	Ω
Internal drain inductance	L _D	Between lead, 6 mm (0.25") from package and center of die contact		-	4.5	-	nH
Internal source inductance	L _S			-	7.5	-	
Drain-Source Body Diode Characteristic	s						
Continuous source-drain diode current	I _S	MOSFET symbol showing the integral reverse p - n junction diode		-	-	8.1	
Pulsed diode forward current ^a	I _{SM}			-	-	32	A
Body diode voltage	V _{SD}	$T_{\rm J} = 25~{}^{\circ}{\rm C},~I_{\rm S} = 8.1~{\rm A},~V_{\rm GS} = 0~{\rm V}^{\rm b}$		-	-	2.0	V
Body diode reverse recovery time	t _{rr}	$T_J = 25 \text{ °C}, I_F = 5.6 \text{ A}, dI/dt = 100 \text{ A/}\mu\text{s}^{\text{b}}$		-	220	440	ns
Body diode reverse recovery charge	Q _{rr}			-	1.2	2.4	μC
Forward turn-on time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by Ls and L				L _D)	

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)

b. Pulse width $\leq 300~\mu s;~duty~cycle \leq 2~\%$

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

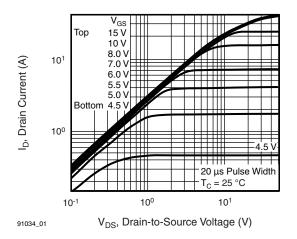


Fig. 1 - Typical Output Characteristics, T_C = 25 °C

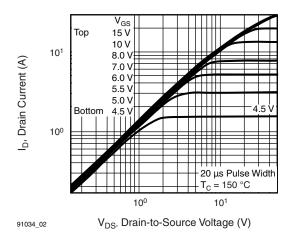
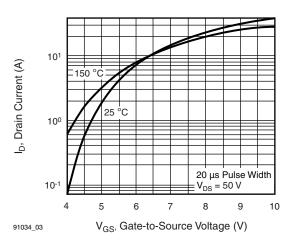


Fig. 2 - Typical Output Characteristics, $T_C = 150 \ ^{\circ}C$





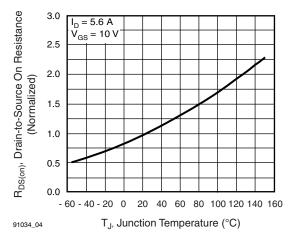


Fig. 4 - Normalized On-Resistance vs. Temperature

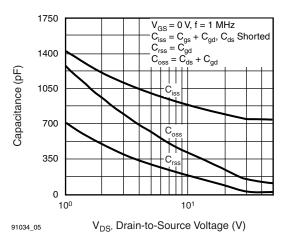
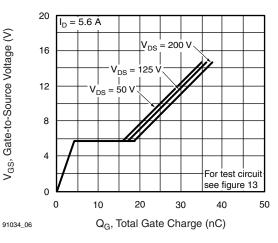


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage



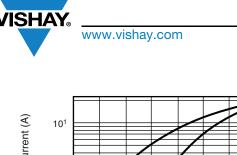


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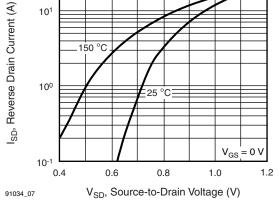


Fig. 7 - Typical Source-Drain Diode Forward Voltage

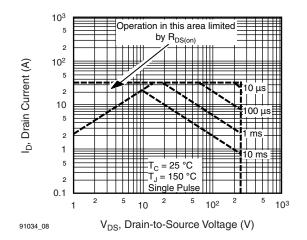


Fig. 8 - Maximum Safe Operating Area

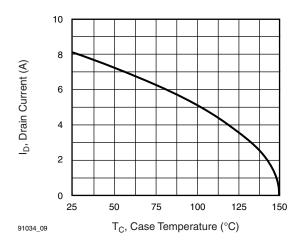


Fig. 9 - Maximum Drain Current vs. Case Temperature

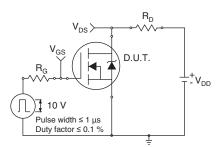


Fig. 10a - Switching Time Test Circuit

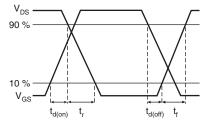


Fig. 10b - Switching Time Waveforms

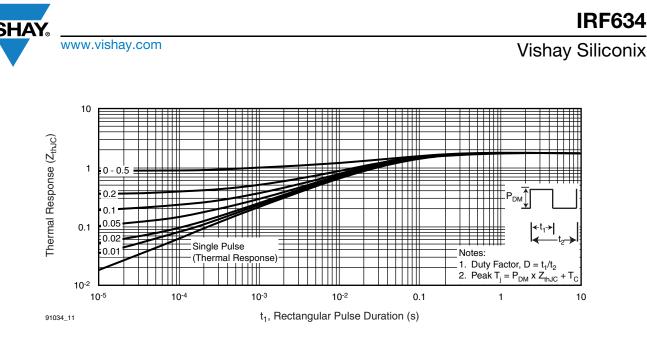
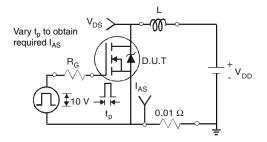


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case



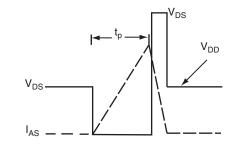


Fig. 12a - Unclamped Inductive Test Circuit

Fig. 12b - Unclamped Inductive Waveforms

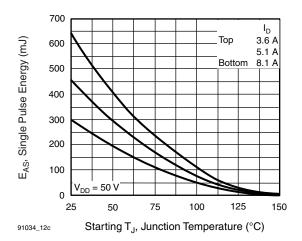
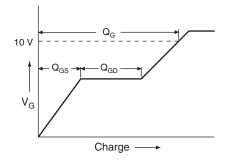


Fig. 12c - Maximum Avalanche Energy vs. Drain Current



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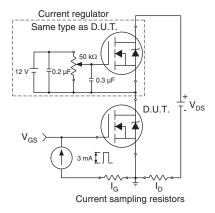


Fig. 13a - Basic Gate Charge Waveform



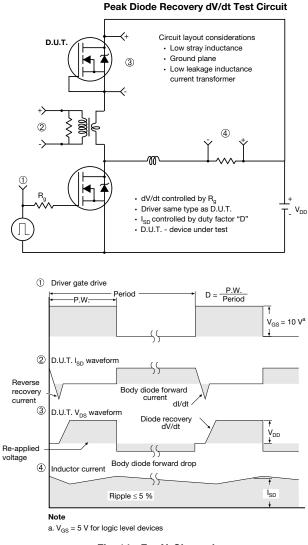


Fig. 14 - For N-Channel

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