**IRF634** 

Vishay Siliconix



**TO-220AB** 

**PRODUCT SUMMARY** 

V<sub>DS</sub> (V)

R<sub>DS(on)</sub> (Ω)

Q<sub>gs</sub> (nC)

Q<sub>gd</sub> (nC)

Q<sub>a</sub> max. (nC)

Configuration

# **Power MOSFET**

S

N-Channel MOSFET

0.45

250

41

6.5

22

Single

 $V_{GS} = 10 V$ 

### FEATURES

- Dynamic dV/dt rating
- Repetitive avalanche rated
- Fast switching
- Ease of paralleling
- Simple drive requirements



#### Note

This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

### DESCRIPTION

Third generation power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free	IRF634PbF

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub>	,			1 15417		
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-source voltage			V <sub>DS</sub>	250	V	
Gate-source voltage			V <sub>GS</sub>	± 20		
Continuous drain current	V <sub>GS</sub> at 10 V	T <sub>C</sub> = 25 °C T <sub>C</sub> = 100 °C	1-	8.1		
		T <sub>C</sub> = 100 °C	ID	5.1	A	
Pulsed drain current <sup>a</sup>			I <sub>DM</sub>	32	]	
Linear derating factor				0.59	W/°C	
Single pulse avalanche energy <sup>b</sup>			E <sub>AS</sub>	300	mJ	
Repetitive avalanche current <sup>a</sup>			I <sub>AR</sub>	8.1	А	
Repetitive avalanche energy <sup>a</sup>			E <sub>AR</sub>	7.4	mJ	
Maximum power dissipation	T <sub>C</sub> = 25 °C		PD	74	W	
Peak diode recovery dV/dt <sup>c</sup>			dV/dt	4.8	V/ns	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	*0		
Soldering recommendations (peak temperature) <sup>d</sup>	For	10 s		300	°C	
	6-32 or M3 screw			10	lbf ∙ in	
Mounting torque				1.1	N ⋅ m	

#### Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)

- b.  $V_{DD}$  = 50 V, starting T<sub>J</sub> = 25 °C, L = 7.3 mH, R<sub>g</sub> = 25  $\Omega$ , I<sub>AS</sub> = 8.1 A (see fig. 12)
- c.  $I_{SD} \le 8.1$  A, dI/dt  $\le 120$  A/µs,  $V_{DD} \le V_{DS}$ ,  $T_J \le 150$  °C

d. 1.6 mm from case

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1 For technical questions, contact: <u>hvm@vishay.com</u>



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THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	TYP.	MAX.	UNIT	
Maximum junction-to-ambient	R <sub>thJA</sub>	-	62		
Case-to-sink, flat, greased surface	R <sub>thCS</sub>	0.50	-	°C/W	
Maximum junction-to-case (drain)	R <sub>thJC</sub>	-	1.7		

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static					•	•	
Drain-source breakdown voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA		250	-	-	V
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_J$	Reference	Reference to 25 °C, $I_D = 1 \text{ mA}$		0.37	-	V/°C
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		2.0	-	4.0	V
Gate-source leakage	I <sub>GSS</sub>	$V_{GS} = \pm 20 V$		-	-	± 100	nA
		V <sub>DS</sub> = 250 V, V <sub>GS</sub> = 0 V		-	-	25	μA
Zero gate voltage drain current	IDSS	V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C		-	-	250	
Drain-source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 5.1 A <sup>b</sup>	-	-	0.45	Ω
Forward transconductance	9 <sub>fs</sub>	V <sub>DS</sub> = 50 V, I <sub>D</sub> = 5.1 A <sup>b</sup>		1.6	-	-	S
Dynamic		•				•	
Input capacitance	C <sub>iss</sub>	$V_{GS} = 0 V,$ $V_{DS} = 25 V,$ f = 1.0 MHz, see fig. 5		-	770	-	pF
Output capacitance	C <sub>oss</sub>			-	190	-	
Reverse transfer capacitance	C <sub>rss</sub>			-	52	-	
Total gate charge	Qg	V <sub>GS</sub> = 10 V	$I_D = 5.6 \text{ A}, V_{DS} = 200 \text{ V},$ see fig. 6 and 13 <sup>b</sup>	-	-	41	nC
Gate-source charge	Q <sub>gs</sub>			-	-	6.5	
Gate-drain charge	Q <sub>gd</sub>			-	-	22	
Turn-on delay time	t <sub>d(on)</sub>			-	9.6	-	ns
Rise time	t <sub>r</sub>	V <sub>DD</sub> = 1	V <sub>DD</sub> = 125 V, I <sub>D</sub> = 5.6 A,		21	-	
Turn-off delay time	t <sub>d(off)</sub>	$R_g = 12 \Omega$ , $R_D = 22 \Omega$ , see fig. 10 <sup>b</sup>		-	42	-	
Fall time	t <sub>f</sub>			-	19	-	
Gate input resistance	Rg	f = 1 MHz, open drain		0.6	-	2.9	Ω
Internal drain inductance	L <sub>D</sub>	Between lead, 6 mm (0.25") from package and center of die contact		-	4.5	-	nH
Internal source inductance	L <sub>S</sub>			-	7.5	-	
Drain-Source Body Diode Characteristic	s						
Continuous source-drain diode current	I <sub>S</sub>	MOSFET symbol showing the integral reverse p - n junction diode		-	-	8.1	
Pulsed diode forward current <sup>a</sup>	I <sub>SM</sub>			-	-	32	A
Body diode voltage	V <sub>SD</sub>	$T_{\rm J} = 25~{}^{\circ}{\rm C},~I_{\rm S} = 8.1~{\rm A},~V_{\rm GS} = 0~{\rm V}^{\rm b}$		-	-	2.0	V
Body diode reverse recovery time	t <sub>rr</sub>	$T_J = 25 \text{ °C}, I_F = 5.6 \text{ A}, dI/dt = 100 \text{ A/}\mu\text{s}^{\text{b}}$		-	220	440	ns
Body diode reverse recovery charge	Q <sub>rr</sub>			-	1.2	2.4	μC
Forward turn-on time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by Ls and L				L <sub>D</sub> )	

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)

b. Pulse width  $\leq 300~\mu s;~duty~cycle \leq 2~\%$ 

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## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

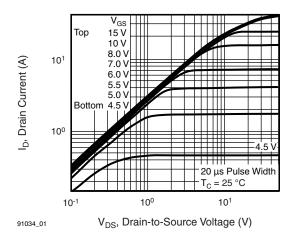


Fig. 1 - Typical Output Characteristics, T<sub>C</sub> = 25 °C

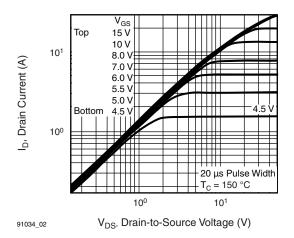
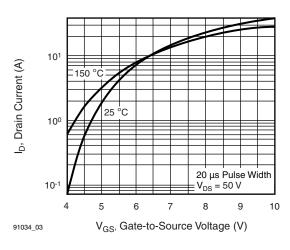


Fig. 2 - Typical Output Characteristics,  $T_C = 150 \ ^{\circ}C$ 





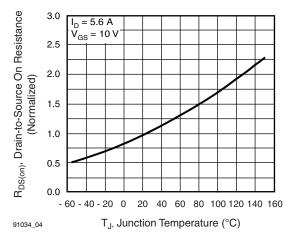


Fig. 4 - Normalized On-Resistance vs. Temperature

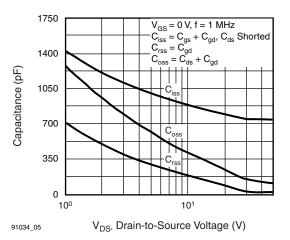
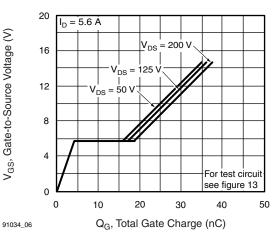


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage



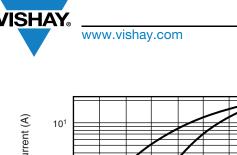


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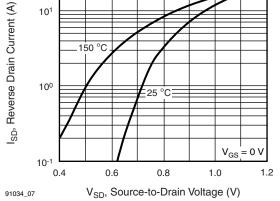


Fig. 7 - Typical Source-Drain Diode Forward Voltage

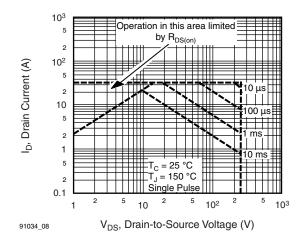


Fig. 8 - Maximum Safe Operating Area

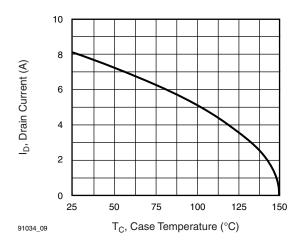


Fig. 9 - Maximum Drain Current vs. Case Temperature

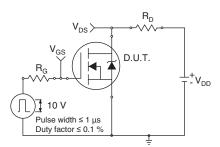


Fig. 10a - Switching Time Test Circuit

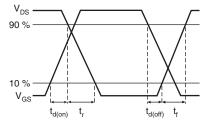


Fig. 10b - Switching Time Waveforms

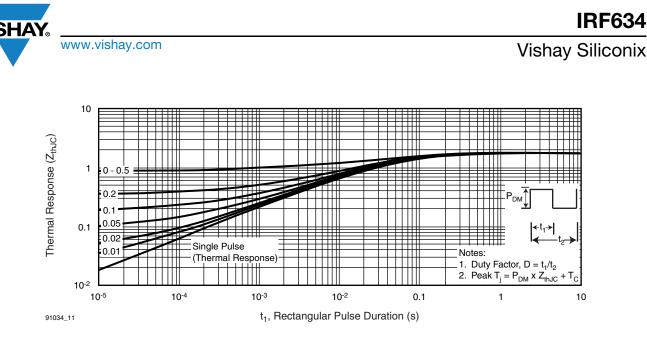
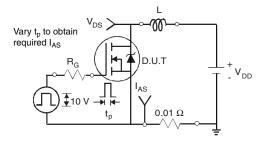


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case



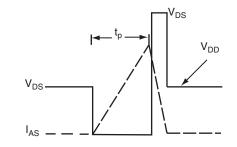


Fig. 12a - Unclamped Inductive Test Circuit

Fig. 12b - Unclamped Inductive Waveforms

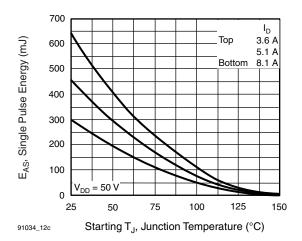
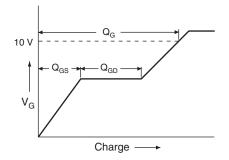


Fig. 12c - Maximum Avalanche Energy vs. Drain Current



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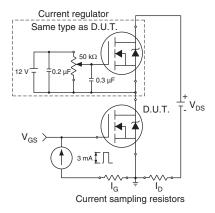
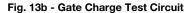


Fig. 13a - Basic Gate Charge Waveform



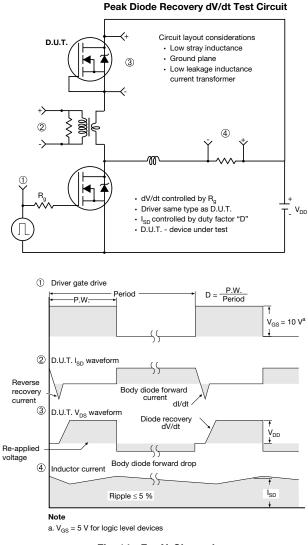


Fig. 14 - For N-Channel

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