VS-VSUD360CW40

Vishay Semiconductors



FRED Pt[®] Ultrafast Soft Recovery Diode Module, 360 A



360 A

400 V

243 nC

74 ns

Modules - diode, FRED Pt®

TO-244

Two diodes common cathode

PRIMARY CHARACTERISTICS

I_{F(AV)}

 V_R

Q_{rr} (typical)

t_{rr}

Type

Package

Circuit configuration

- Very low Q_{rr} and t_{rr}
- UL approved file E222165
- · Designed and qualified for industrial level



 Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- Reduced RFI and EMI
- Higher frequency operation
- Reduced snubbing

DESCRIPTION / APPLICATIONS

FRED Pt[®] diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are a significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V _R		400	V	
Continuous forward current per diode	I _{F(AV)}	T _C = 25 °C	510		
		T _C = 85 °C	305	A	
		T _C = 116 °C	180		
Single pulse forward current per diode	I _{FSM}	T _C = 25 °C	2880		
	P _D	T _C = 25 °C	570	W	
Maximum power dissipation		T _C = 110 °C	180	vv	
Operating junction and storage temperatures	T _J , T _{Stg}		-40 to +150	°C	

ELECTRICAL SPECIFICATIONS PER LEG ($T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	L TEST CONDITIONS MIN. T		TYP.	MAX.	UNITS
Breakdown voltage	V _{BR}	V _{BR} I _R = 100 μA		-	-	
Famueral voltance		I _F = 180 A	-	1.09	1.27	
		I _F = 360 A	-	1.23	1.50	V
Forward voltage	V _{FM}	I _F = 180 A, T _J = 150 °C	-	0.88	0.96	
		I _F = 360 A, T _J = 150 °C	-	1.04	1.18	
Reverse leakage current	I _{RM}	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	0.26	1.28	mA
Series inductance	L _S	From top of terminal hole to mounting plane - 5 -		-	nH	

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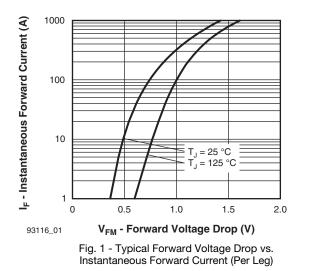
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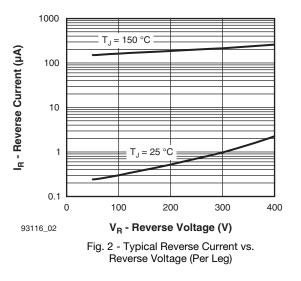


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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
Reverse recovery time t _{rr}		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}$	-	40	69		
	t _{rr}	T _J = 25 °C	I _F = 180 A, dI _F /dt = 200 A/μs,	-	74	-	ns
		T _J = 150 °C	$V_{\rm R} = 200 \text{ V}$	-	171	-	
Peak recovery current	I _{RRM}	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	5.1	-	
		I _F = 180 A, dI _F /dt = 200 A/μs, V _R = 200 V		-	6.6	-	А
		$I_F = 180 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 200 \text{ V}, \text{ T}_J = 150 \ ^\circ\text{C}$		-	15.2	-	
	Q _{rr}	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	125	-	
Reverse recovery charge		$I_F = 180 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 200 \text{ V}$		-	243	-	nC
		I _F = 180 A, dI _F /dt = 200 A	/μs, V _R = 200 V, T _J = 150 °C	-	1295	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction-to-case per leg	P		-	-	0.19	
Thermal resistance, junction-to-case per module	R _{thJC}		-	-	0.095	°C/W
Thermal resistance, case-to-heatsink (flag greased surface)	R _{thCS}		-	0.10	-	
Weight			-	68	-	g
			-	2.4	-	oz.
Mounting torque			30 (3.4)	-	40 (4.6)	llaf in
Mounting torque center hole			12 (1.4)	-	18 (2.1)	lbf · in (N · m)
Terminal torque			30 (3.4)	-	40 (4.6)	(11 · 11)
Vertical pull			-	-	80	llaf in
2" lever pull			-	-	35	lbf∙in
Case style			TO-244			





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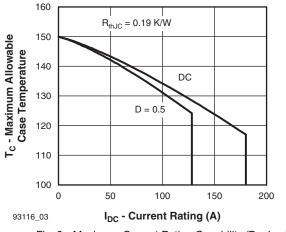
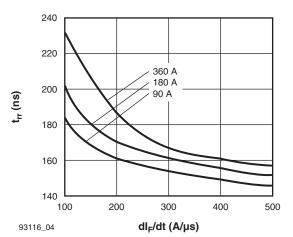
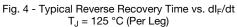


Fig. 3 - Maximum Current Rating Capability (Per Leg)



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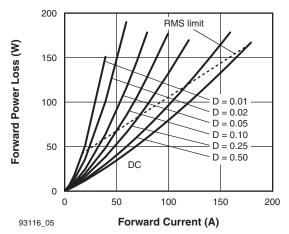


Fig. 5 - Forward Power Loss Characteristics

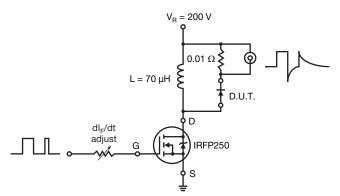
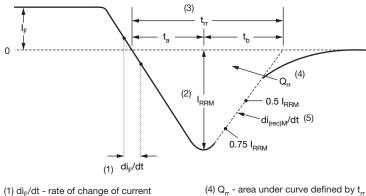


Fig. 6 - Reverse Recovery Parameter Test Circuit

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through zero crossing

and I_{RRM}

(2) I_{RRM} - peak reverse recovery current

(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

 $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$

(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 7 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

www.vishay.com

Device code	VS-VS	UD	360	С	W	40
	1	2	3	4	5	6
	1 - 2 -		ay Semi		•	
	3 -	Curre	ent ratin	g (360 =	= 360 A))
	4 -		uit config			
	5 -	Туре	wo diod of devio TO-244	ce:		hode not insulate
	6 -	Volta	ige ratin	g (40 =	400 V)	

CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Two diodes common cathode	С	Lug terminal anode 2 Lug terminal anode 1			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			

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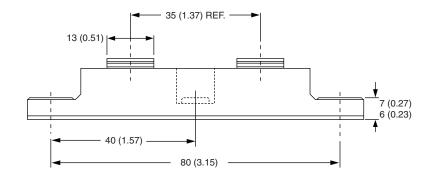


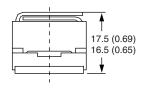


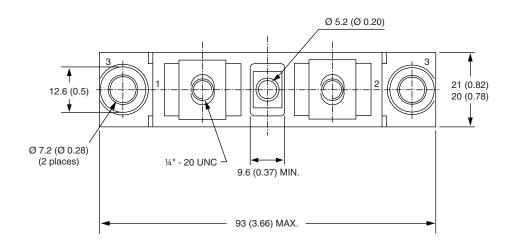
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TO-244

DIMENSIONS in millimeters (inches)









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