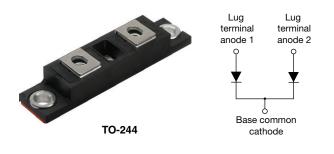
VS-HFA320NJ40CPbF

Vishay Semiconductors

HEXFRED[®] Ultra Fast Soft Recovery Diode, 320 A



www.vishay.com

FEATURES

- Very low Q_{rr} and t_{rr}
- UL approved file E222165



 Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- Reduced RFI and EMI
- Reduced snubbing

DESCRIPTION / APPLICATIONS

HEXFRED[®] diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. An extensive characterization of the recovery behavior for different values of current, temperature and dl_F/dt simplifies the calculations of losses in the operating conditions. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for power converters, motors drives and other applications where switching losses are significant portion of the total losses.

PRIMARY CHARACTERISTICS				
I _{F(AV)}	320 A			
V _R	400 V			
$I_{F(DC)}$ at T_C	255 A at 85 °C			
Package	TO-244			
Circuit configuration	Two diodes common cathode			

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL TEST CONDITIONS		MAX.	UNITS	
Cathode to anode voltage	V _R		400	V	
		T _C = 25 °C	420		
Continuous forward current	I _F	T _C = 85 °C	255		
		T _C = 115 °C	160	A	
Single pulse forward current	I _{FSM}	Limited by junction temperature	1200		
Non-repetitive avalanche energy	E _{AS}	L = 100 μ H, duty cycle limited by maximum T _J	1.4	mJ	
Maximum accuration D		T _C = 25 °C	625		
Maximum power dissipation	PD	T _C = 100 °C	250	W	
Operating junction and storage temperature range	T _J , T _{Stg}		-55 to +150	°C	

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA		400	-	-	
		I _F = 160 A		-	1.10	1.35	V
Maximum forward voltage	V _{FM}	I _F = 320 A	See fig. 1	-	1.30	1.54	
		I _F = 160 A, T _J = 125 °C		-	1.00	1.20	
Maximum reverse leakage current	I _{RM}	T _J = 125 °C, V _R = 400 V	See fig. 2	-	0.9	3	mA
Junction capacitance	CT	V _R = 200 V	See fig. 3	-	370	500	pF
Series inductance	L _S	From top of terminal hole to mounting plane		-	5.0	-	nH

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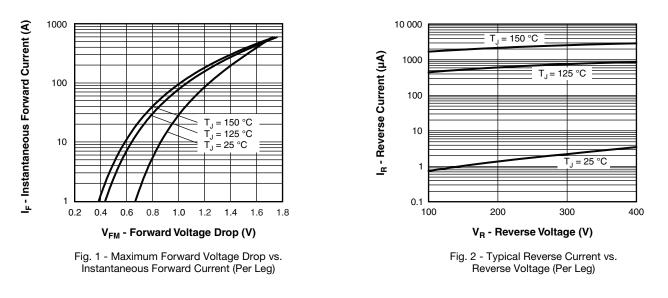
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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	45	-		
Reverse recovery time t _{rr}	t _{rr}	T _J = 25 °C	I _F = 160 A dI⊧/dt = 200 A/µs	-	90	140	ns	
		T _J = 125 °C		-	290	440		
Peak recovery current See fig. 6	I _{RRM}	T _J = 25 °C		-	8.7	20	А	
		T _J = 125 °C		-	18	30	A	
Reverse recovery charge		Q _{rr}	T _J = 25 °C	$V_{\rm R} = 200 \text{ V}$	-	420	1100	nC
See fig. 7			T _J = 125 °C		-	2600	7000	nc
Peak rate of recovery current See fig. 8	dl _{(rec)M} /dt	مال (مال	T _J = 25 °C		-	300	-	A /uo
		T _J = 125 °C		-	280	-	A/µs	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}	- 55	-	150	°C	
Thermal resistance, junction to case	per leg	– R _{thJC}	-	-	0.19	°C/W K/W	
	per module	L thJC	-	-	0.095		
Typical thermal resistance, case to heatsink		R _{thCS}	-	0.10	-		
Weight			-	68	-	g	
			-	2.4	-	oz.	
Mounting torgue ⁽¹⁾			30 (3.4)	-	40 (4.6)		
Mounting torque (*)	center hole		12 (1.4)	-	18 (2.1)	lbf · in (N · m)	
Terminal torque			30 (3.4)	-	40 (4.6)	()	
Vertical pull			-	-	80	lbf · in	
2" lever pull			-	-	35		

Note

(1) Mounting surface must be smooth, flat, free of burrs or other protrusions. Apply a thin even film or thermal grease to mounting surface. Gradually tighten each mounting bolt in 5 to 10 lbf · in steps until desired or maximum torque limits are reached.



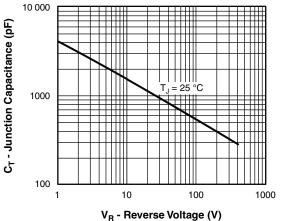
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VR - neverse voltage (V)

Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

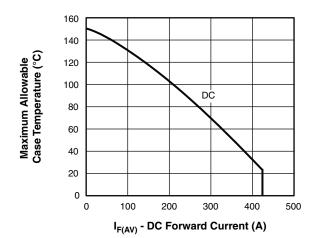


Fig. 4 - Maximum Allowable Case Temperature vs. DC Forward Current (Per Leg)

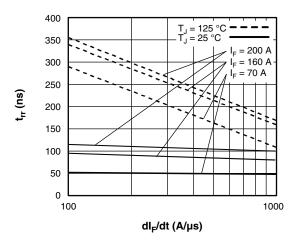


Fig. 5 - Typical Reverse Recovery Time vs. dl_F/dt (Per Leg)

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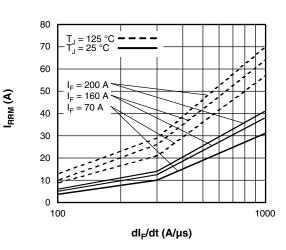
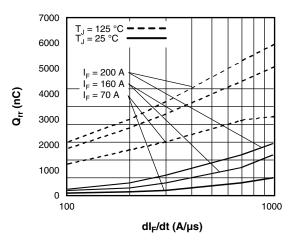
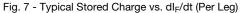


Fig. 6 - Typical Recovery Current vs. dl_F/dt (Per Leg)





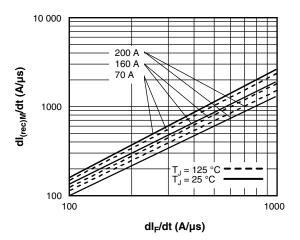


Fig. 8 - Typical dI(rec)M/dt vs. dIF/dt (Per Leg)

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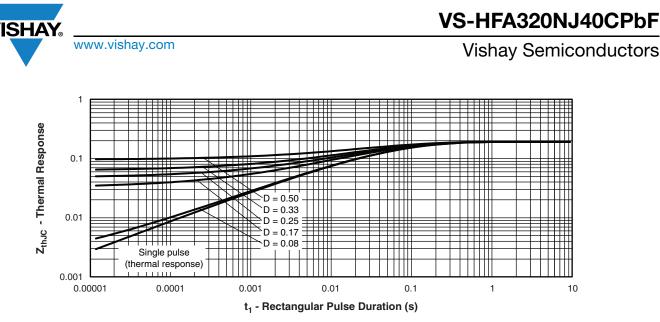


Fig. 9 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

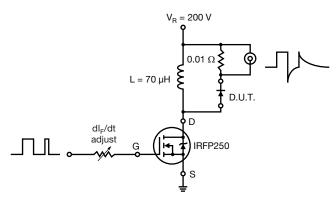
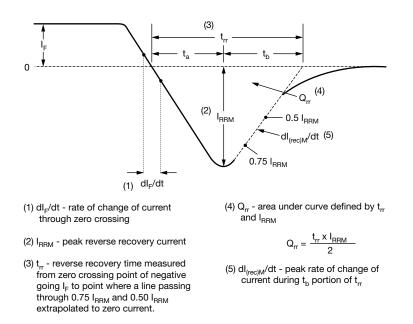
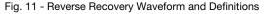


Fig. 10 - Reverse Recovery Parameter Test Circuit





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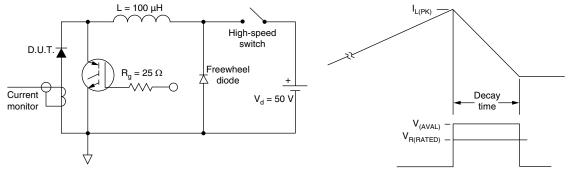
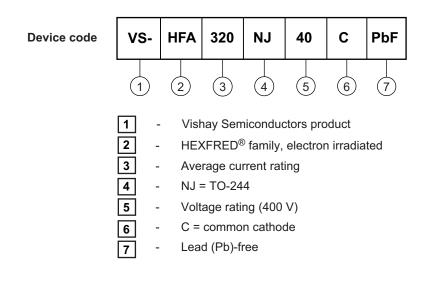


Fig. 12 - Avalanche Test Circuit and Waveforms

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LINKS TO RELATED DOCUMENTS				
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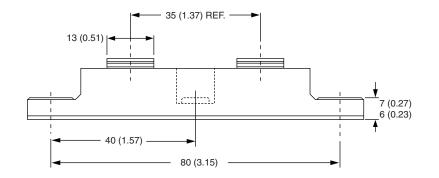


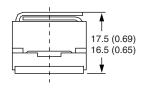


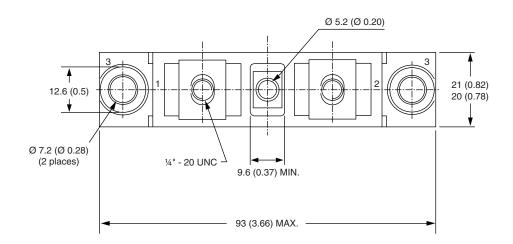
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TO-244

DIMENSIONS in millimeters (inches)









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