

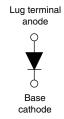
High Performance Schottky Rectifiers, 120 A



HALF-PAK (D-67)

Package

Circuit configuration



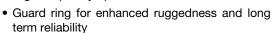
HALF-PAK (D-67)

Single diode

PRIMARY CHARACTERISTICS					
I _{F(AV)}	120 A				
V _R	15 V				

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- · Low forward voltage drop
- High frequency operation



- Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-125NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	UNITS			
I _{F(AV)}	Rectangular waveform	120	Α		
V _{RRM}		15	V		
I _{FSM}	t _p = 5 μs sine	10 800	Α		
V _F	120 A _{pk} , T _J = 125 °C	0.37	V		
T _J	Range	-55 to +125	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-125NQ015PbF	UNITS		
Maximum DC reverse voltage	V_{R}	15	- V		
Maximum working peak reverse voltage	V _{RWM}	25			

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 74 °C, rectangular waveform		120	
Maximum peak one cycle non-repetitive surge current See fig. 7	lane.	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	10 800	A
	IFSM	10 ms sine or 6 ms rect. pulse		1700	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 5 A, L = 1 mH		12	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ sr Frequency limited by T _J maximum V _A = 1.5 x V _R typical		2	А



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	. TEST CONDITIONS VA		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	120 A	T _J = 25 °C	0.43	V
		240 A		0.58	
		120 A	T _J = 75 °C	0.37	
		240 A		0.52	
Maximum reverse leakage current per leg See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	40	mA
		T _J = 100 °C		2000	
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		7700	pF
Typical series inductance	L _S	From top of terminal hole to mounting plane		7.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width $<300~\mu s,~duty~cycle < 2~\%$

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature ran	ige	TJ		-55 to 125	°C	
Maximum storage temperature ran	ge	T _{Stg}		-55 to 150	°C	
Maximum thermal resistance, junct	ion to case	R _{thJC}	DC operation See fig. 4	0.38	°C/W	
Typical thermal resistance, case to	heatsink	R _{thCS}	Mounting surface, smooth and greased	0.05		
Approximate weight				30	g	
				1.06	oz.	
Mounting torque minimum maximum			3 (26.5)			
	maximum		Non-lubricated threads —	4 (35.4)	N · m (lbf · in)	
Terminal torque -	minimum			3.4 (30)		
	maximum			5 (44.2)		
Case style			HALF-PAK m		(module	

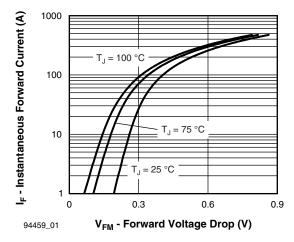


Fig. 1 - Maximum Forward Voltage Drop Characteristics

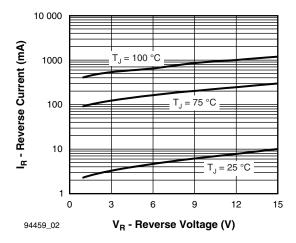


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

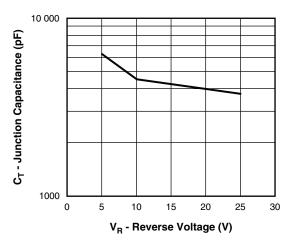


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

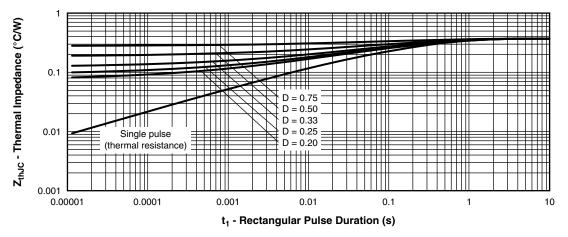


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

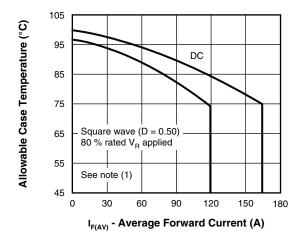


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

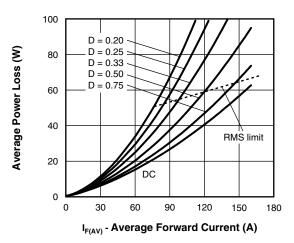
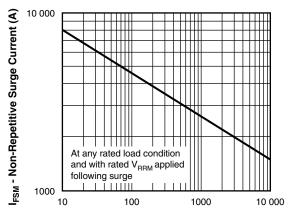


Fig. 6 - Forward Power Loss Characteristics



t_o - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

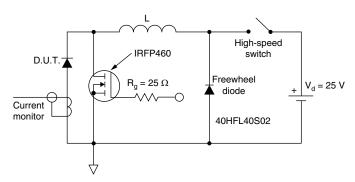


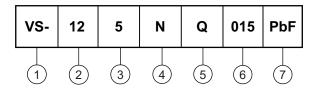
Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{ll} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$

ORDERING INFORMATION TABLE

Device code



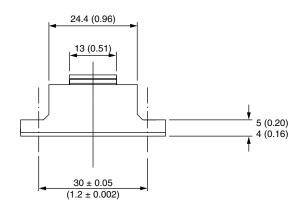
- 1 Vishay Semiconductors product
- Average current rating (x 10)
- Product silicon identification
- 4 N = not isolated
- 5 Q = Schottky rectifier diode
- 6 Voltage rating (015 = 15 V)
- 7 Lead (Pb)-free

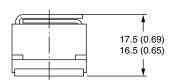
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95020		

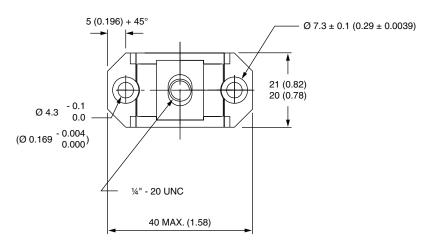


D-67 HALF-PAK

DIMENSIONS in millimeters (inches)









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