VS-VSUD400CW20

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FRED Pt[®] Ultrafast Soft Recovery Diode Module, 480 A



PRIMARY CHARACTERISTICS					
I _{F(AV)}	480 A				
V _R	200 V				
Q _{rr} (typical)	249 nC				
t _{rr}	87 ns				
Туре	Modules - diode, FRED Pt [®]				
Package	TO-244				
Circuit configuration	Two diodes common cathode				

FEATURES

- Ultrafast recovery
- UL approved file E222165
- Designed for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- Reduced RFI and EMI
- Higher frequency operation
- · Reduced snubbing
- · Reduced parts count

DESCRIPTION

FRED Pt[®] diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V _R		200	V	
Continuous forward current	I _{F(AV)}	T _C = 127 °C	240	٨	
Single pulse forward current	I _{FSM}	T _C = 25 °C	2300	A	
Storage temperature range	T _{Stg}		-40 to +150	°C	
Operating junction temperature range	TJ		-40 to +175	°C	

ELECTRICAL SPECIFICATIONS PER LEG ($T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	TEST CONDITIONS MIN. TYP.		MAX.	UNITS
Breakdown voltage	V _{BR}	_{BR} I _R = 100 μA 200 -		-		
Forward voltage V _{FM}		I _F = 200 A	-	0.94	1.0	
	V	I _F = 400 A	-	1.06	1.14	V
	I _F = 200 A, T _J = 175 °C	-	0.73	0.80		
		I _F = 400 A, T _J = 175 °C	-	0.88	0.99	
Reverse leakage current	I _{RRM}	$T_J = 175 \text{ °C}, V_R = V_R \text{ rated}$	-	0.67	1.5	mA
Series inductance	Ls	From top of terminal hole to mounting plane - 5 -		nH		

DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 $^{\circ}$ C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
		I_F = 50 A, dI_F/dt = 200 A/µs, V_R = 200 V, T_J = 25 °C	-	87	130		
Reverse recovery time t _{rr}	۲r	$I_F = 50 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 100 \text{ V}, \text{ T}_J = 125 \ ^\circ\text{C}$	-	95	155	ns	
Peak recovery current	$I_F = 50 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 200 \text{ V}, \text{ T}_J = 25 \ ^\circ\text{C}$	-	6	11.5	А		
	$I_F = 50 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 100 \text{ V}, \text{ T}_J = 125 \ ^\circ\text{C}$	-	10.62	16.5	~		
Reverse recovery charge Q _{rr}	0	$I_F = 50 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 200 \text{ V}, \text{ T}_J = 25 \ ^\circ\text{C}$	-	249	420	nC	
	Q _{rr}	I_F = 50 A, dI _F /dt = 200 A/µs, V_R = 100 V, T_J = 125 °C	-	493	980		

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THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-	-	-40 to +175	°C	
Thermal resistance, junction-to-case per leg	D		-	-	0.19	°C/W	
Thermal resistance, junction-to-case per module	R _{thJC}	DC operation	-	-	0.095		
Thermal resistance, case-to-heatsink (flag greased surface)	R _{thCS}	Flag, greased, surface	-	0.10	-		
			-	68	-	g	
Weight			-	2.4	-	oz.	
Mounting torque			30 (3.4)	-	40 (4.6)		
Mounting torque center hole			12 (1.4)	-	18 (2.1)	lbf · in (N · m)	
Terminal torque			30 (3.4)	-	40 (4.6)	(11 11)	
Vertical pull			-	-	80	lbf∙in	
2" lever pull			-	-	35	חויוטו	
Case style			TO-244				

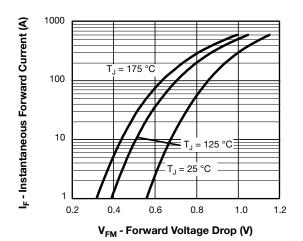
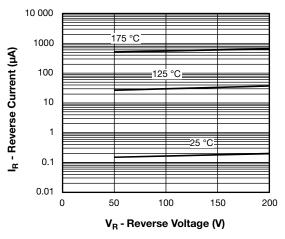
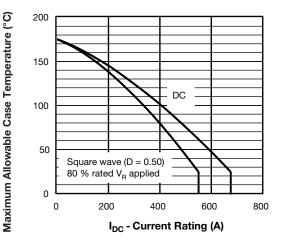


Fig. 1 - Typical Forward Voltage Drop vs. Instantaneous Forward Current (Per Leg)









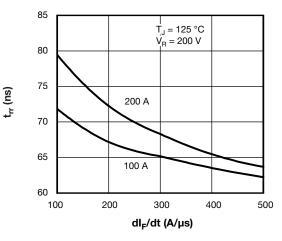


Fig. 4 - Typical Recovery Time vs. dI_F/dt (Per Leg)

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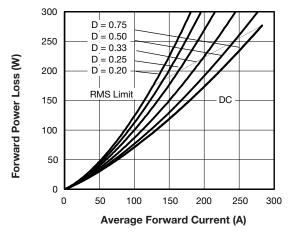


Fig. 5 - Forward Power Loss Characteristics (Per Leg)

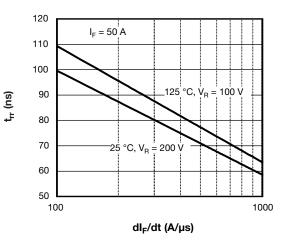
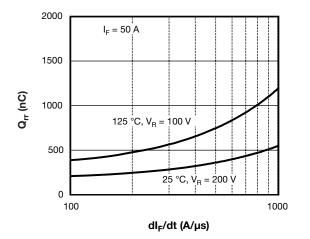


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt (Per Leg)



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Fig. 7 - Typical Reverse Recovery Charge vs. dl_F/dt (Per Leg)

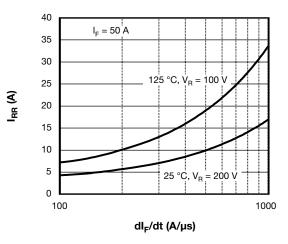


Fig. 8 - Typical Reverse Recovery Current vs. dl_F/dt (Per Leg)

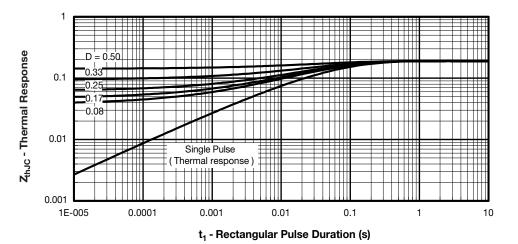


Fig. 9 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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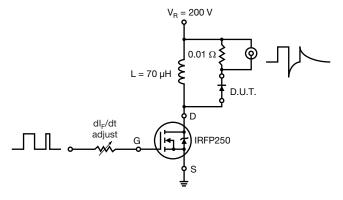


Fig. 10 - Reverse Recovery Parameter Test Circuit (All recovery characteristics have been determined using test circuit shown)

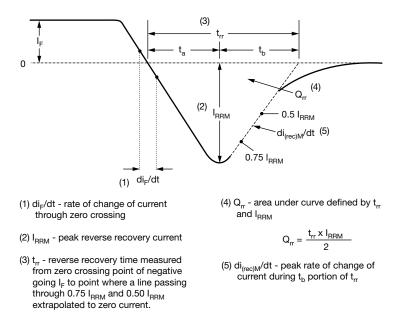


Fig. 11 - Reverse Recovery Waveform and Definitions





ORDERING INFORMATION TABLE

Device code	VS-VS	UD	400	С	w	20
	1	2	3	4	5	6
	1 -	VS-\	/S = Vis	hay Ser	nicondu	ictors pr
	2 -	Ultra	fast diod	de		
	3 -	Curr	ent rating	g (400 =	400 A)	
	4 -	Circu	uit config	juration:		
		C = 1	not isola	ted		
	5 -	Туре	e of devi	ce:		
		W =	TO-244	wire bo	ndable	not isola
	6 -	Volta	age ratin	g (20 =	200 V)	

CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Two diodes common cathode	С	Lug anode 2 Lug terminal terminal anode 1			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			



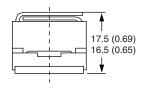


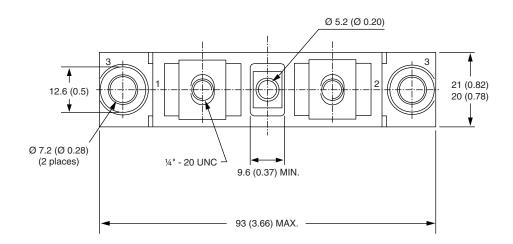
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TO-244

DIMENSIONS in millimeters (inches)









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