# **VS-QA300FA17**

**Vishay Semiconductors** 

## Insulated Gen 2 Schottky Rectifier Module, 300 A



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### ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS						
$I_{F(AV)}$ per module at $T_C = 132 \text{ °C}$	300 A					
V <sub>R</sub>	170 V					
V <sub>FM</sub> at 100 A, T <sub>C</sub> = 25 °C	0.79 V					
Package	SOT-227					
Circuit configuration	Two separate diodes, parallel pin-out					

### **FEATURES**

- Max. T<sub>J</sub> = 175 °C
- Two fully independent diodes
- · Fully insulated package
- Trench MOS Barrier Schottky technology
- Ultra low forward voltage drop
- Optimized for power conversion: welding and industrial SMPS applications
- · Easy to use and parallel
- · Industry standard outline
- · Designed and gualified for industrial level
- UL approved file E78996
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### DESCRIPTION

The VS-QA300FA17 insulated modules integrate two state of the art Trench MOS Schottky technology rectifiers in the compact, industry standard SOT-227 package.

These devices are thus intended for high frequency converters and switching power supplies.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
V <sub>F</sub>	T <sub>J</sub> = 150 °C	0.69	V		
TJ	Range	-55 to +175	C°		

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_C = 25 \text{ °C}$ unless otherwise specified)				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Average forward current per module	I <sub>F(AV)</sub>	T <sub>C</sub> = 132 °C	300	A
Cathode to anode voltage	V <sub>R</sub>		170	V
Continuous forward current per diode	١ <sub>F</sub>	T <sub>C</sub> = 90 °C	330	
Single pulse forward current per diode	I <sub>FSM</sub>	$T_{C} = 175 \text{ °C}, t = 6 \text{ ms}, \text{ square}$	1575	A
Maximum power dissipation per diode	PD	T <sub>C</sub> = 90 °C	327	W
Non-repetitive avalanche energy per diode	E <sub>AS</sub>	$T_J = 25 \text{ °C}, I_{AS} = 27 \text{ A}, L = 10 \text{ mH}$	3700	mJ
RMS isolation voltage	VISOL	Any terminal to case, t = 1 min	2500	V
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C



RoHS COMPLIANT



<b>ELECTRICAL SPECIFICATIONS PER DIODE</b> ( $T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V <sub>BR</sub>	$I_R = 2 \text{ mA}$	170	-	-	
Forward voltage V <sub>FM</sub>		I <sub>F</sub> = 100 A	-	0.79	0.85	v
	$V_{FM}$	I <sub>F</sub> = 100 A, T <sub>J</sub> = 150 °C	-	0.62	-	
		I <sub>F</sub> = 200 A	-	0.89	0.98	
	I <sub>F</sub> = 200 A, T <sub>J</sub> = 150 °C	-	0.75	-		
Reverse leakage current I <sub>RM</sub>	V <sub>R</sub> = 170 V	-	13	200	μA	
	IRM	T <sub>J</sub> = 125 °C, V <sub>R</sub> = 170 V	-	20	-	mA
Junction capacitance	CT	V <sub>R</sub> = 170 V	-	737	-	pF

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time t <sub>rr</sub>	+	$T_J = 25 \ ^\circ C$	I <sub>F</sub> = 50 A di <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 100 V	-	71	-	ns A
	۲rr	T <sub>J</sub> = 125 °C		-	82	-	
Peak recovery current I <sub>RRM</sub>		T <sub>J</sub> = 25 °C		-	7.1	-	
	IRRM	T <sub>J</sub> = 125 °C		-	8.8	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	252	-	nC
		T <sub>J</sub> = 125 °C		-	352	-	110

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction-to-case, single leg conducting			-	-	0.26	
Junction-to-case, both leg conducting	R <sub>thJC</sub>		-	-	0.13	°C/W
Case-to-heatsink	R <sub>thCS</sub>	Flat, greased surface	-	0.1	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				SC	)T-227	



## VS-QA300FA17

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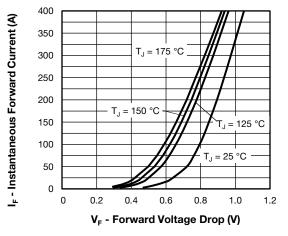


Fig. 1 - Typical Forward Voltage Drop vs. Instantaneous Forward Current (Per Diode)

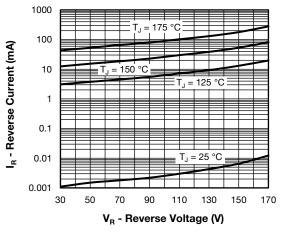


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Diode)

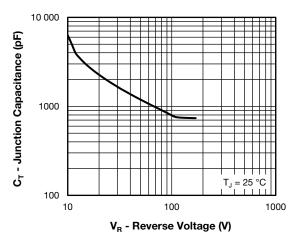


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Diode)

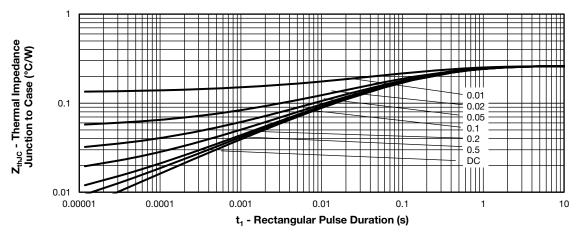
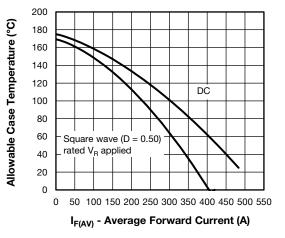


Fig. 4 - Maximum Thermal Impedance Junction-to-Case Characteristics (Per Diode)

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Fig. 5 - Maximum Current Rating Capability (Per Diode)

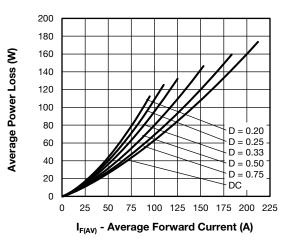


Fig. 6 - Forward Power Loss Characteristics (Per Diode)

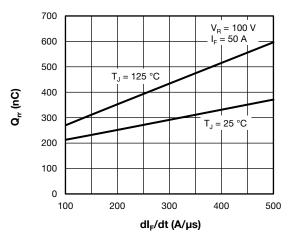


Fig. 7 - Typical Reverse Recovery Charge vs dl<sub>F</sub>/dt (Per Diode)

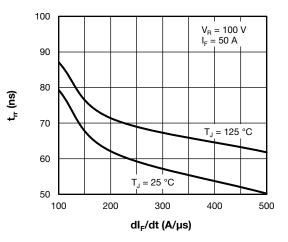


Fig. 8 - Typical Reverse Recovery Time vs dl<sub>F</sub>/dt (Per Diode)

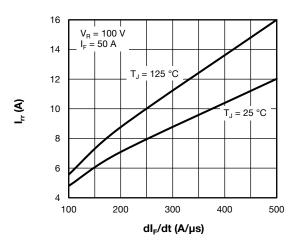


Fig. 9 - Typical Reverse Recovery Current vs dIFdt (Per Diode)

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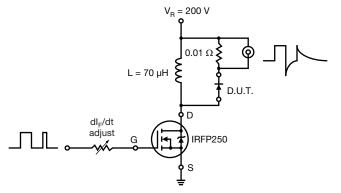


Fig. 10 - Reverse Recovery Parameter Test Circuit

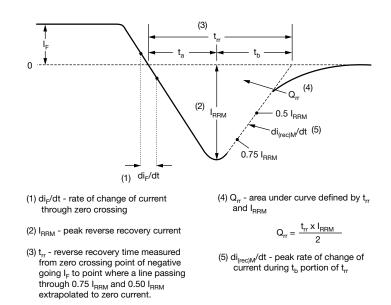
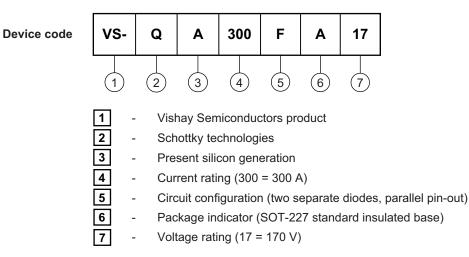


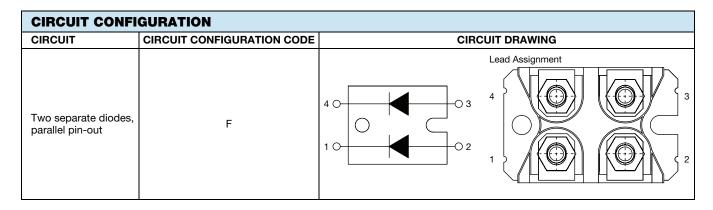
Fig. 11 - Reverse Recovery Waveform and Definitions



### **ORDERING INFORMATION TABLE**



Quantity per tube is 10, M4 screw and washer included

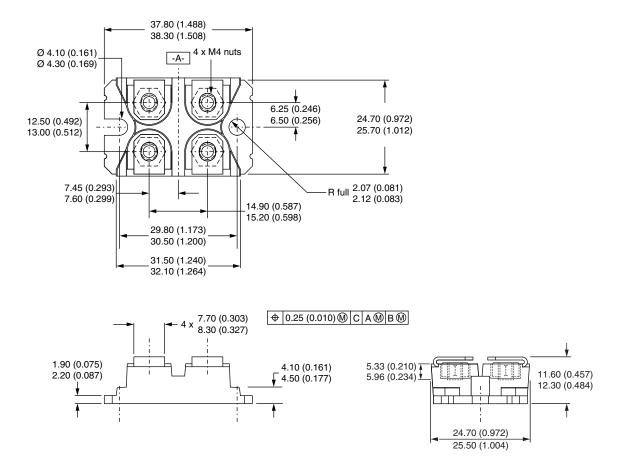


LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95423					
Part marking information	www.vishay.com/doc?95425				



SOT-227 Generation 2

#### **DIMENSIONS** in millimeters (inches)



#### Note

• Controlling dimension: millimeter



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