

RoHS

COMPLIANT

HALOGEN FREE

Hyperfast Rectifier, 15 A FRED Pt®



TO-220 FullPAK 2L



VS-15ETH06FP-N3

PRIMARY CHARACTERISTICS				
I _{F(AV)}	15 A			
V _R	600 V			
V _F at I _F	1.3 V			
t _{rr} typ.	22 ns			
T _J max.	175 °C			
Package	TO-220 FullPAK 2L			
Circuit configuration	Single			

FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- · Low leakage current
- Single die center tap module
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V _{RRM}		600	V	
Average rectified forward current	I _{F(AV)}	T _C = 80 °C	15		
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	180	Α	
Peak repetitive forward current	I _{FM}		30		
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS MIN.		TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	$I_R = 100 \ \mu A$	600	-	-	
Forward voltage	V	I _F = 15 A	-	1.8	2.2	V
Forward voltage V _F	I _F = 15 A, T _J = 150 °C	-	1.3	1.6		
Poverse leakage current		$V_R = V_R$ rated	-	0.2	50	
Reverse leakage current I _R		$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	30	500	μA
Junction capacitance	C _T	V _R = 600 V		20	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8.0 -		nΗ		



DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		I _F = 1 A, dI _F /dt = 100 A/μs, V _R = 30 V		-	22	30	
Barrage was a series of the se	+	$I_F = 15 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	28	35	no
Reverse recovery time	t _{rr}	T _J = 25 °C	I _F = 15 A · dI _F /dt = 200 A/μs · V _B = 390 V	-	29	-	ns
		T _J = 125 °C		-	75	-	
Deal massing a summer		T _J = 25 °C		-	3.5	-	А
Peak recovery current	I _{RRM}	T _J = 125 °C		-	7	-	
Reverse recovery charge		T _J = 25 °C	VR = 000 V	-	57	-	nC
Reverse recovery charge Q _{rr}	T _J = 125 °C		-	300	-		
Reverse recovery time	t _{rr}	T _J = 125 °C	I _F = 15 A dI _F /dt = 800 A/μs V _R = 390 V	-	51	-	ns
Peak recovery current	I _{RRM}			-	20	-	Α
Reverse recovery charge	Q_{rr}			-	580	-	nC

THERMAL MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction-to-case	R _{thJC}		-	3.0	3.5	
Thermal resistance, junction-to-ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W
Thermal resistance, case-to-heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2.0	-	g
vveignt			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220 FullPAK 2L	15ETH06FP			

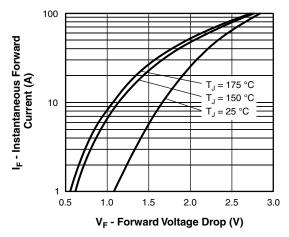


Fig. 1 - Typical Forward Voltage Drop Characteristics

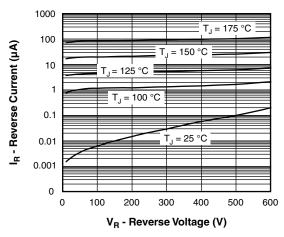


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

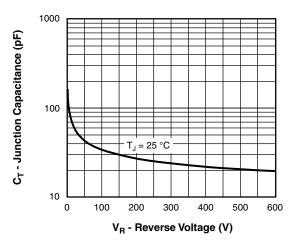


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

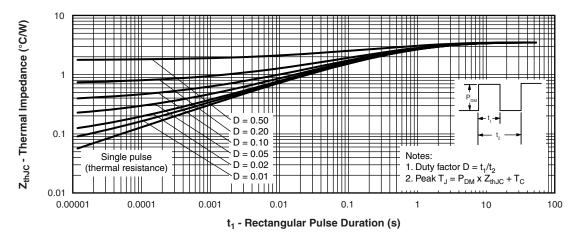


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

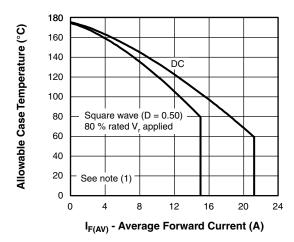


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

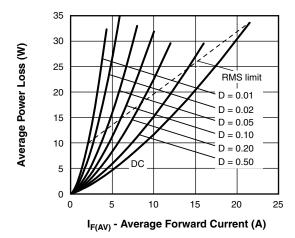


Fig. 6 - Forward Power Loss Characteristics

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times R_{\text{thJC}}; \\ \text{Pd} = & \text{forward power loss} = I_{\text{F(AV)}} \times V_{\text{FM}} \text{ at } (I_{\text{F(AV)}}/D) \text{ (see fig. 5)}; \\ \text{Pd}_{\text{REV}} = & \text{inverse power loss} = V_{\text{R1}} \times I_{\text{R}} \text{ (1 - D)}; I_{\text{R}} \text{ at } V_{\text{R1}} = \text{rated } V_{\text{R}} \\ \end{array}$



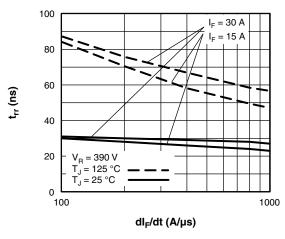


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

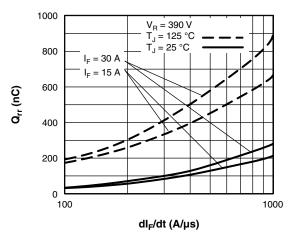
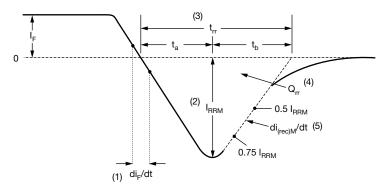


Fig. 8 - Typical Stored Charge vs. dl_F/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) ${\rm Q}_{\rm rr}$ area under curve defined by ${\rm t}_{\rm rr}$ and ${\rm I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} x I_{RRM}}{2}$$

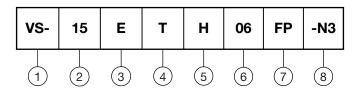
(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Current rating (15 = 15 A)

3 - E = single

4 - T = TO-220, D^2PAK (TO-263AB)

5 - H = hyperfast recovery

6 - Voltage rating (06 = 600 V)

7 - FP = TO-220 FullPAK 2L

8 - Environmental digit:

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

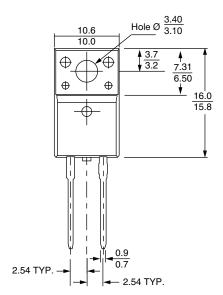
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-15ETH06FP-N3	50	1000	Antistatic plastic tube		

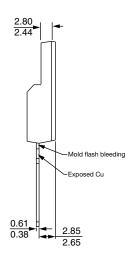
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96157			
Part marking information	www.vishay.com/doc?95392			
SPICE model	www.vishay.com/doc?96618			

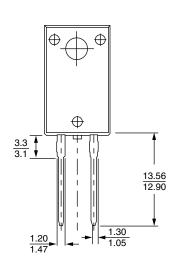


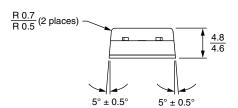
2L TO-220 FullPAK

DIMENSIONS in millimeters









Bottom view



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Vishay

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