# VS-VSKDF500/06PbF

Vishay Semiconductors

## FRED Pt<sup>®</sup> Gen 4 Doubler Ultrafast Diode, 500 A (INT-A-PAK Power Modules)



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### **FEATURES**

- Gen 4 FRED Pt<sup>®</sup> dices technology
- Ultrasoft reverse recovery characteristics
- Low I<sub>BBM</sub> and reverse recovery charge
- Very low forward voltage drop
- 175 °C operating junction temperature
- UL approved file E78996 for application with maximum case temperature up to 140 °C
- Large creepage distances
- Designed and gualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### DESCRIPTION

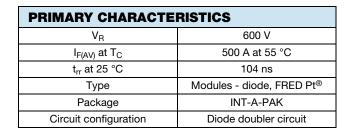
Gen 4 FRED Pt technology, state of the art, ultra low V<sub>F</sub>, soft switching optimized for IGBT F/W diode.

The minimized conduction loss, optimized storage charge, and low recovery current, minimized the switching losses and reduce the over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Cathode to anode voltage	V <sub>R</sub>		600	V			
Continuous forward current	1	T <sub>C</sub> = 25 °C	772				
Continuous forward current	١ <sub>F</sub>	T <sub>C</sub> = 90 °C	519	А			
Single pulse forward current	I <sub>FSM</sub>	$t_p$ = 10 ms, 50 Hz, sine half wave, initial T <sub>J</sub> = 175 °C	4140				
Maximum power dissipation	PD	T <sub>C</sub> = 25 °C	1363	w			
	PD	T <sub>C</sub> = 90 °C	772	vv			
Operating junction temperature range	TJ		-40 to +175	°C			
Storage temperature range	T <sub>Stg</sub>		-40 to +150	C			
RMS insulation voltage	V <sub>INS</sub>	50 Hz, circuit to base, all terminals shorted, t = 1 s	3500	V			

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Cathode to anode breakdown voltage	V <sub>BR</sub>	I <sub>R</sub> = 500 μA	600	-	-		
Forward voltage drop	V <sub>FM</sub>	I <sub>F</sub> = 250 A	-	1.25	-	V	
		I <sub>F</sub> = 500 A	-	1.45	1.66		
		I <sub>F</sub> = 250 A, T <sub>J</sub> = 150 °C	-	1.06	-		
		I <sub>F</sub> = 500 A, T <sub>J</sub> = 150 °C	-	1.35	-		
Reverse leakage current	I <sub>RM</sub>	V <sub>R</sub> = 600 V	-	10	-	μA	
		T <sub>J</sub> = 150 °C, V <sub>R</sub> = 600 V	-	2.5	-	mA	

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 150 A dl/dt = 1000 A/μs V <sub>R</sub> = 400 V	-	104	-	ns
		T <sub>J</sub> = 125 °C		-	193	-	
Peak recovery current	I <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	59	-	Α μC
		T <sub>J</sub> = 125 °C		-	122	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	3.5	-	
		T <sub>J</sub> = 125 °C		-	13.8	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum therm junction to case		R <sub>thJC</sub>	DC operation	0.11	K/W	
Typical thermal resistance, case to heat sink		R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	0.035	N/W	
Mounting torque ± 10 %to heat sinkbusbar			A mounting compound is recommended and the			
			torque should be rechecked after a period of 3 hours to allow the spread of the compound	4 to 6	Nm	
Approximatowa	ight			200	g	
Approximate weight				7.1	oz.	
Case style				INT-A-PAK		

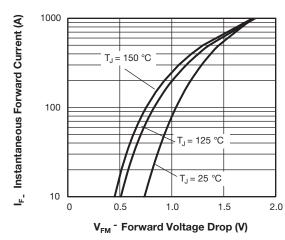


Fig. 1 - Typical Forward Voltage Drop Characteristics

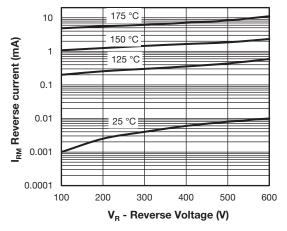


Fig. 2 - Typical Value of Reverse Current vs. Reverse Voltage



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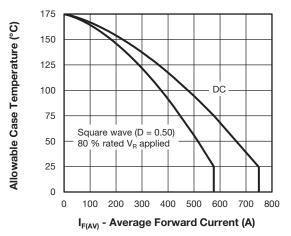


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current

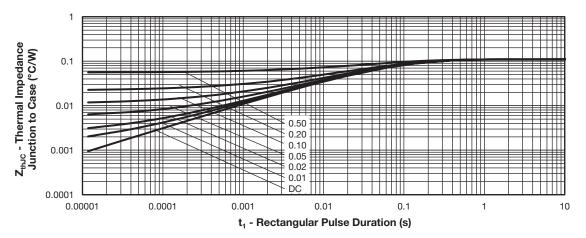


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

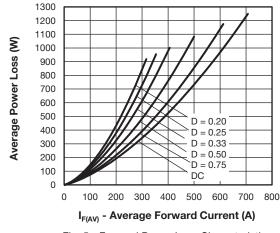
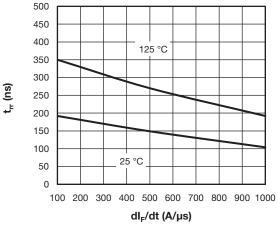
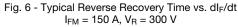


Fig. 5 - Forward Power Loss Characteristics





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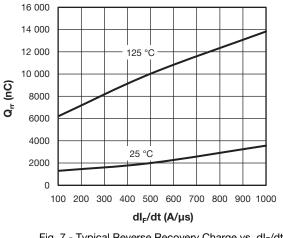


Fig. 7 - Typical Reverse Recovery Charge vs. dl<sub>F</sub>/dt  $I_{FM} = 150 \text{ A}, V_{R} = 300 \text{ V}$ 

#### **ORDERING INFORMATION TABLE**

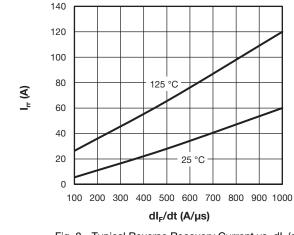


Fig. 8 - Typical Reverse Recovery Current vs. dl<sub>F</sub>/dt I<sub>FM</sub> = 150 A, V<sub>R</sub> = 300 V

Device code	VS-VS	KD	F	500	06	PbF	
	1	2	3	4	5	6	
	1 -   2 -   3 -   4 -   5 -   6 -	Circu F = F Curre Volta	ay Semi lit config RED Pt ent ratin lige ratin = lead (l	uration: <sup>®</sup> ultrafa g (500 = g (06 =	KD = d ist diode 500 A) 600 V)	oubler c e	ircuit

CIRCUIT CONFIGURATION						
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING				
Diode doubler circuit	KD	KD reversed polarity				

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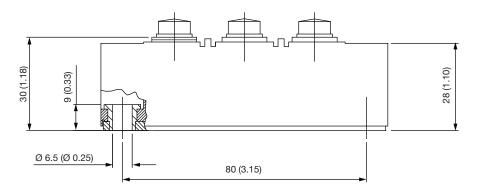
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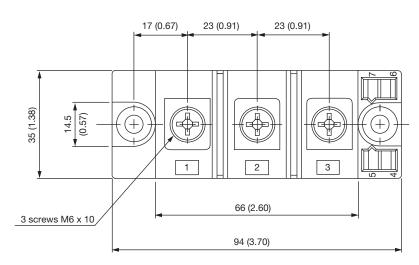


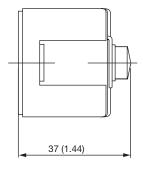
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## Vishay Semiconductors

**DIMENSIONS** in millimeters (inches)







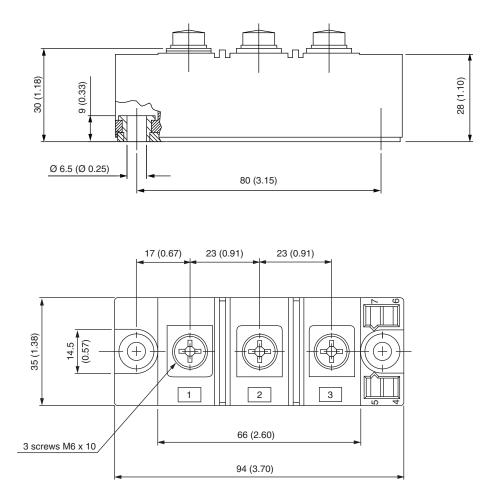


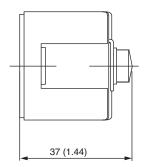
# **Outline Dimensions**

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## **INT-A-PAK DBC**

### **DIMENSIONS** in millimeters (inches)







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