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### Vishay Semiconductors

# "Full Bridge" IGBT MTP (TrenchStop IGBT), 57 A



(Package example)

PRIMARY CHARACTERISTICS					
V <sub>CES</sub>	1200 V				
I <sub>C</sub> at T <sub>C</sub> = 25 °C	57 A				
V <sub>CE(on)</sub>	1.84 V				
Speed	8 kHz to 30 kHz				
Package	MTP				
Circuit configuration	Full bridge				

#### **FEATURES**

- Trench and Field Stop IGBT technology
- Positive V<sub>CE(on)</sub> temperature coefficient
- 10 µs short circuit capability
- HEXFRED® antiparallel diodes with ultrasoft reverse recovery
- Low diode V<sub>F</sub>
- Square RBSOA
- Aluminum nitride DBC
- Very low stray inductance design for high speed operation
- UL approved file E78996
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **BENEFITS**

- Optimized for welding, UPS and SMPS applications
- Rugged with ultrafast performance
- Outstanding ZVS and hard switching operation
- Low EMI, requires less snubbing
- · Excellent current sharing in parallel operation
- Direct mounting to heatsink
- PCB solderable terminals
- Very low junction to case thermal resistance

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter breakdown voltage	V <sub>CES</sub>		1200	V	
Ocation of a literature of the state of the		T <sub>C</sub> = 25 °C	57		
Continuous collector current	I <sub>C</sub>	T <sub>C</sub> = 80 °C	42		
Pulsed collector current	I <sub>CM</sub>	$T_J = 150  ^{\circ}\text{C},  t_p = 6  \text{ms},  V_{GE} = 15  \text{V}$	50		
Clamped inductive load current	I <sub>LM</sub>		75	A	
Diode continuous forward current	I <sub>F</sub>	T <sub>C</sub> = 106 °C	25	1	
Diode maximum forward current	I <sub>FM</sub>		100		
Gate to emitter voltage	V <sub>GE</sub>		± 20	V	
RMS isolation voltage	V <sub>ISOL</sub>	Any terminal to case, t = 1 min	2500	]	
Maximum newer dissipation (only ICPT)	В	T <sub>C</sub> = 25 °C	240	W	
Maximum power dissipation (only IGBT)	$P_{D}$	T <sub>C</sub> = 80 °C	134	v	



<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V <sub>(BR)CES</sub>	$V_{GE} = 0 \text{ V}, I_{C} = 850  \mu\text{A}$	1200	-	-	V
		V <sub>GE</sub> = 15 V, I <sub>C</sub> = 20 A	-	1.84	2.16	
		V <sub>GE</sub> = 15 V, I <sub>C</sub> = 40 A	-	2.60	-	
Collector to emitter saturation voltage	V <sub>CE(on)</sub>	CE(CIT)	2.06	-	V	
			3.19	-		
		V <sub>GE</sub> = 15 V, I <sub>C</sub> = 20 A, T <sub>J</sub> = 150 °C	-	2.12	-	
Gate threshold voltage	V <sub>GE(th)</sub>	$V_{CE} = V_{GE}, I_{C} = 850 \mu A$	4.7	5.8	6.8	
Temperature coefficient of threshold voltage	$V_{GE(th)}/\Delta T_J$	$V_{CE} = V_{GE}$ , $I_{C} = 0.85$ mA (25 °C to 125 °C)	-	-12.1	-	mV/°C
Transconductance	9 <sub>fe</sub>	V <sub>CE</sub> = 20 V, I <sub>C</sub> = 20 A	-	13	-	S
Zero gate voltage collector current		V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V, T <sub>J</sub> = 25 °C	-	1.0	200	μA
	I <sub>CES</sub> <sup>(1)</sup>	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V, T <sub>J</sub> = 125 °C	-	0.52	-	A
		V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V, T <sub>J</sub> = 150 °C	-	2.1	-	mA
Gate to emitter leakage current	I <sub>GES</sub>	V <sub>GE</sub> = ± 20 V	-	-	± 250	nA

#### Note

 $<sup>^{(1)}</sup>$   $I_{\text{CES}}$  includes also opposite leg overall leakage

<b>SWITCHING CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg	I <sub>C</sub> = 20 A	-	119	-	
Gate to emitter charge (turn-on)	Q <sub>ge</sub>	V <sub>CC</sub> = 960 V	-	20	-	nC
Gate to collector charge (turn-on)	$Q_{gc}$	V <sub>GE</sub> = 15 V	-	57	-	
Turn-on switching loss	E <sub>on</sub>	$V_{CC} = 600 \text{ V}, I_{C} = 20 \text{ A}, V_{GE} = 15 \text{ V},$	-	0.75	-	- mJ
Turn-off switching loss	E <sub>off</sub>	$R_g = 5 \Omega$ , L = 1 mH, $T_J = 25 ^{\circ}$ C, energy losses include tail and	-	0.66	-	
Total switching loss	E <sub>tot</sub>	diode reverse recovery	-	1.41	-	
Turn-on switching loss	E <sub>on</sub>	$V_{CC}$ = 600 V, $I_{C}$ = 20 A, $V_{GE}$ = 15 V, $R_{g}$ = 5 $\Omega$ , L = 1 mH, $T_{J}$ = 125 °C, energy losses include tail and diode reverse recovery	-	1.08	_	
Turn-off switching loss	E <sub>off</sub>		-	1.18	-	
Total switching loss	E <sub>tot</sub>		-	2.26	-	
Input capacitance	C <sub>ies</sub>	V <sub>GF</sub> = 0 V	-	1430	-	
Output capacitance	C <sub>oes</sub>	V <sub>CC</sub> = 30 V	-	115	-	рF
Reverse transfer capacitance	C <sub>res</sub>	f = 1.0 MHz	-	75	-	
Reverse bias safe operating area	RBSOA	$\begin{split} T_J &= 150~^{\circ}\text{C}, \\ I_C &= 75~\text{A},  V_{CC} = 900~\text{V},  V_p = 1200~\text{V}, \\ R_g &= 4.7~\Omega,  V_{GE} = +~15~\text{V} \text{ to 0 V}, \\ L &= 500~\mu\text{H} \end{split}$	Fullsquare			
Short circuit safe operating area	SCSOA	$T_J = 150  ^{\circ}\text{C},$ $V_{CC} = 800  \text{V},  V_p = 1200  \text{V},$ $R_g = 5  \Omega,  V_{GE} = +15  \text{V} \text{ to 0 V}$		-	10	μs

<b>DIODE SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Diode forward voltage drop		I <sub>C</sub> = 20 A	-	2.48	2.94	
		I <sub>C</sub> = 40 A	- 3.	3.28	-	
	$V_{FM}$	$V_{FM}$ $I_C = 20 \text{ A, } T_J = 125 ^{\circ}\text{C}$ - 2.44 $I_C = 40 ^{\circ}\text{A, } T_J = 125 ^{\circ}\text{C}$ - 3.45	2.44	-	V	
			3.45	-		
		I <sub>C</sub> = 20 A, T <sub>J</sub> = 150 °C	-	2.21	-	1
Reverse recovery energy of the diode	E <sub>rec</sub>	$V_{GE} = 15 \text{ V}, R_{g} = 5 \Omega, L = 200 \mu\text{H}$	-	420	-	μJ
Diode reverse recovery time	t <sub>rr</sub>	$V_{CC} = 600 \text{ V}, \ I_{C} = 20 \text{ A}$	-	98	-	ns
Peak reverse recovery current	I <sub>rr</sub>	T <sub>J</sub> = 125 °C	=	33	-	Α



THERMAL AND MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS	
Junction and storage temperange	erature	T <sub>J</sub> , T <sub>Stg</sub>		-40	-	150	°C	
Junction to case	IGBT	R <sub>thJC</sub>		1	-	0.52	°C/W	
Junction to case	Diode	□thJC		-	-	0.61		
Case to sink per module		R <sub>thCS</sub>		-	0.06	-		
Clearance			External shortest distance in air between 2 terminals	5.5	-	-		
Creepage			Shortest distance along external surface of the insulating material between 2 terminals	8	-	-	mm	
Mounting torque			A mounting compound is recommended and the torque should be checked after 3 hours to allow for the spread of the compound. Lubricated threads.	r 3 ± 10 %		Nm		
Weight					66		g	

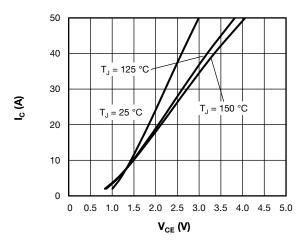


Fig. 1 - Typical Trench IGBT Output Characteristics,  $V_{\text{GE}} = 15 \text{ V}$ 

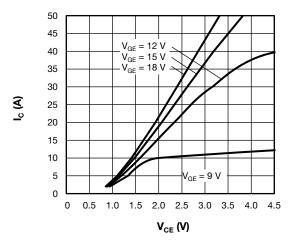


Fig. 2 - Typical Trench IGBT Output Characteristics,  $T_J$  = 125 °C

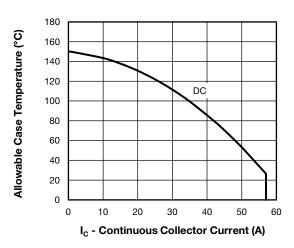


Fig. 3 - Maximum Trench IGBT Continuous Collector Current vs.

Case Temperature

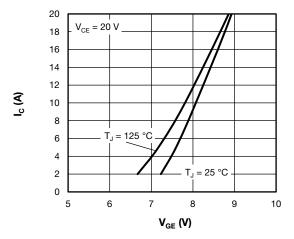


Fig. 4 - Typical Trench IGBT Transfer Characteristics



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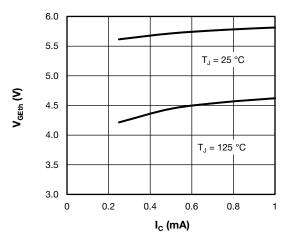


Fig. 5 - Typical Trench IGBT Gate Threshold Voltage

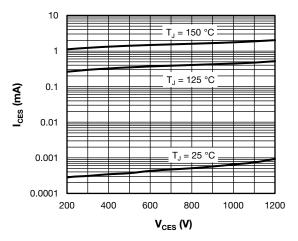


Fig. 6 - Typical Trench IGBT Zero Gate Voltage Collector Current

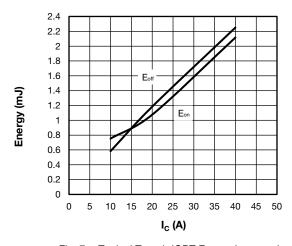


Fig. 7 - Typical Trench IGBT Energy Loss vs. I<sub>C</sub> (with Antiparallel Diode)  $T_J = 125~^{\circ}C, \, V_{CC} = 600~V, \, R_g = 4.7~\Omega, \, V_{GE} = +15V/-15V, \, L = 500~\mu H$ 

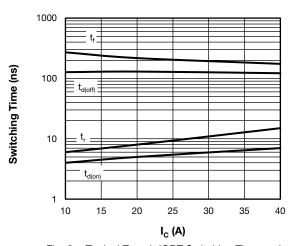


Fig. 8 - Typical Trench IGBT Switching Time vs. I<sub>C</sub> (with Antiparallel Diode) T<sub>J</sub> = 125 °C, V<sub>CC</sub> = 600 V, R<sub>g</sub> = 4.7  $\Omega$ , V<sub>GE</sub> = +15V/-15V, L = 500  $\mu$ H

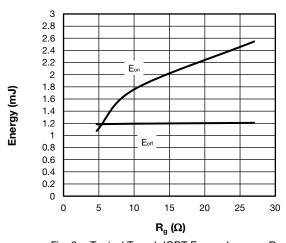


Fig. 9 - Typical Trench IGBT Energy Loss vs.  $R_g$  (with Antiparallel Diode)  $T_J$  = 125 °C,  $V_{CC}$  = 600 V,  $I_C$  = 20 A,  $V_{GE}$  = +15V/-15V, L = 500  $\mu H$ 

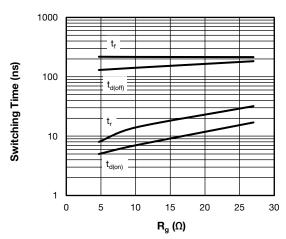


Fig. 10 - Typical Trench IGBT Switching Time vs.  $R_g$  (with Antiparallel Diode)  $T_J$  = 125 °C,  $V_{CC}$  = 600 V,  $I_C$  = 20 A,  $V_{GE}$  = +15V/-15V, L = 500  $\mu H$ 

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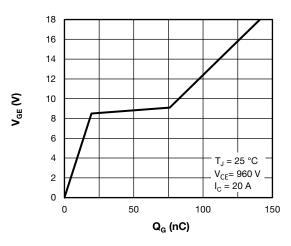


Fig. 11 - Typical Trench IGBT Gate charge vs.
Gate to Emitter Voltage

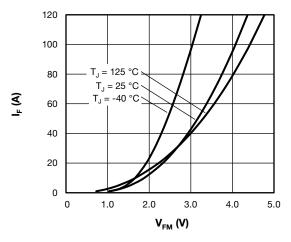


Fig. 12 - Typical Diode Forward Characteristics  $t_p = 80~\mu s$ 

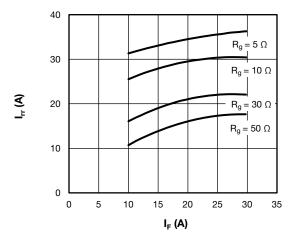


Fig. 13 - Typical Diode  $I_{rr}$  vs.  $I_{F}$ ,  $T_{J} = 150 \, ^{\circ}\text{C}$ 

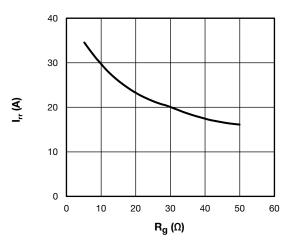


Fig. 14 - Typical Diode  $I_{rr}$  vs.  $R_g$   $T_J = 150~{}^{\circ}\text{C}; I_F = 5.0~\text{A}$ 

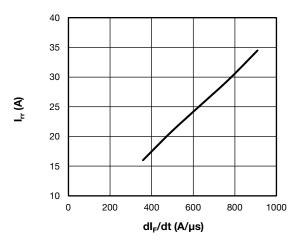


Fig. 15 - Typical Diode  $I_{rr}$  vs.  $dI_F/dt$   $V_{CC}$  = 400 V;  $V_{GE}$  = 15 V;  $I_{CE}$  = 5.0 A;  $T_J$  = 150 °C

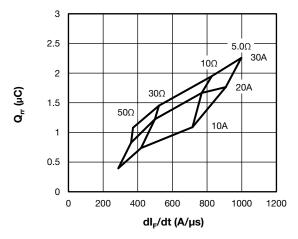


Fig. 16 - Typical Diode  $Q_{rr}$  vs.  $dI_F/dt$  $V_{CC} = 400 \text{ V}$ ;  $V_{GE} = 15 \text{ V}$ ;  $T_J = 150 \text{ °C}$ 



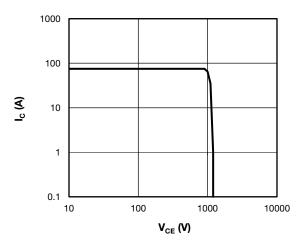


Fig. 17 - Trench IGBT Reverse BIAS SOA  $T_J$  = 150 °C,  $I_C$  = 75 A,  $R_g$  = 4.7  $\Omega,$   $V_{GE}$  = +15V/0 V,  $V_{CC}$  = 700 V,  $V_p$  = 1200 V

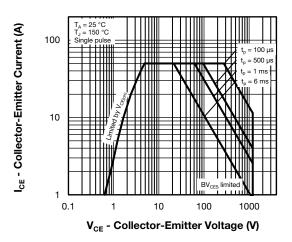


Fig. 18 - Trench IGBT Safe Operating Area

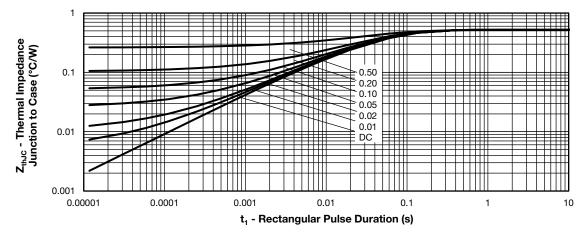


Fig. 19 - Maximum Trench IGBT Thermal Impedance Z<sub>thJC</sub> Characteristics

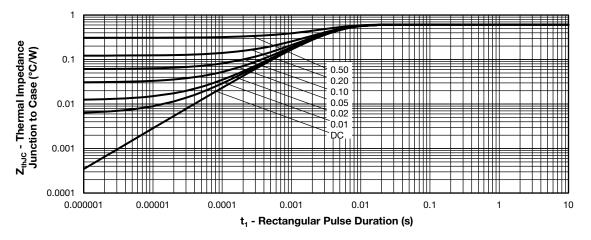
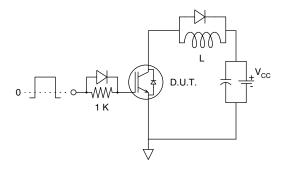


Fig. 20 - Maximum Diode Thermal Impedance Z<sub>thJC</sub> Characteristics





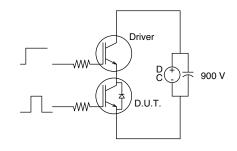
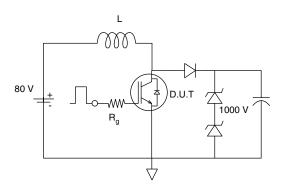


Fig. 21 - Gate Charge Circuit (Turn-Off)

Fig. 23 - S.C. SOA Circuit



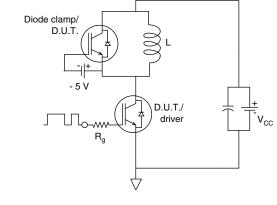
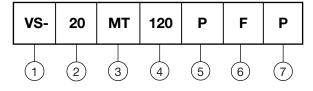


Fig. 22 - RBSOA Circuit

Fig. 24 - Switching Loss Circuit

### **ORDERING INFORMATION TABLE**

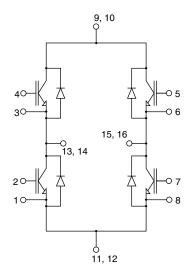
#### Device code



- Vishay Semiconductors product
- 2 Current rating (20 = 20 A)
- 3 Essential part number
- 4 Voltage code (120 = 1200 V)
- 5 Speed / type (P = Trench IGBT)
- Circuit configuration (F = full bridge)
- **7** P = lead (Pb)-free



### **CIRCUIT CONFIGURATION**

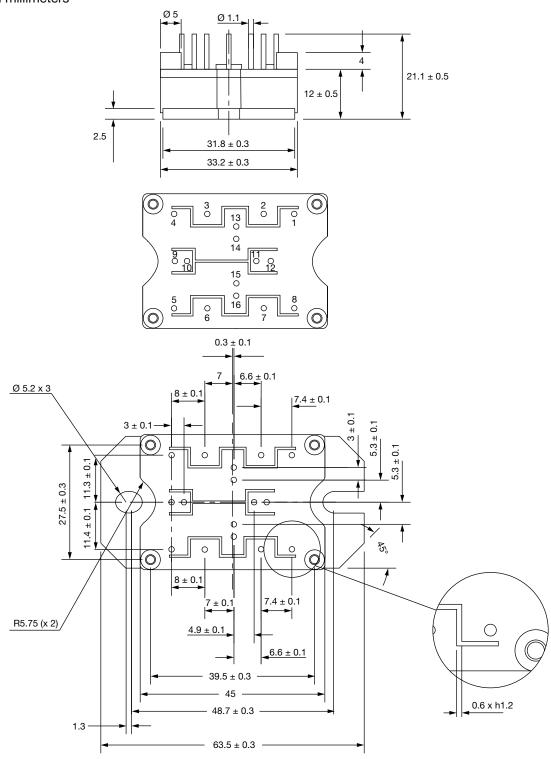


LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95245			



# MTP MOSFET / IGBT Full-Bridge

### **DIMENSIONS** in millimeters



Tolerance (unless other stated):

 $X = \pm 0.3$ 

 $X.X = \pm 0.1$ 

 $X.XX = \pm 0.03$ 



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