

Vishay Semiconductors

"Low Side Chopper" IGBT SOT-227 (Trench IGBT), 47 A



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PRIMARY CHARACTERISTICS					
V _{CES}	1200 V				
I _C DC	50 A at 73 °C				
V _{CE(on)} typical at 50 A, 25 °C	2.39 V				
Package	SOT-227				
Circuit configuration	Low side chopper				

FEATURES

- Trench IGBT technology
- Square RBSOA
- HEXFRED® clamping diode
- Positive V_{CE(on)} temperature coefficient
- · Fully isolated package
- Speed 8 kHz to 60 kHz
- Very low internal inductance (≤ 5 nH typical)
- Industry standard outline
- UL approved file E78996



• Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

BENEFITS

- Designed for increased operating efficiency in power conversion: UPS, SMPS, welding, induction heating
- Easy to assemble and parallel
- · Direct mounting on heatsink
- Plug-in compatible with other SOT-227 packages
- Low EMI, requires less snubbing

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
Continuos collector comment		T _C = 25 °C	68		
Continuous collector current	Ic	T _C = 80 °C	47		
Pulsed collector current	I _{CM}	$T_J = 150 ^{\circ}\text{C}, T_p = 6 \text{ms}, V_{GE} = 15 \text{V}$	150		
Clamped inductive load current	I _{LM}		250	Α	
Diode continuous forward current	I _F	T _C = 25 °C	87	<u></u>	
		T _C = 80 °C	59		
Single pulse forward current	I _{FSM}	10 ms sine or 6 ms rectangular pulse, T _J = 25 °C	310		
Gate to emitter voltage	V _{GE}		± 20	V	
Davis dia dia atia a IODT	Б	T _C = 25 °C	291		
Power dissipation, IGBT	P _D	T _C = 80 °C	163	٦ ,,,	
Device dissipation diada	В	T _C = 25 °C	338	W	
Power dissipation, diode	P _D	T _C = 80 °C	190		
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V	



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{BR(CES)}	$V_{GE} = 0 \text{ V, } I_{C} = 2 \text{ mA}$	1200	-	-		
		V _{GE} = 15 V, I _C = 25 A	-	1.95	-	V	
Collector to amittar valtage	.,	V _{GE} = 15 V, I _C = 50 A	-	2.39	2.8		
Collector to emitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 25 A, T _J = 125 °C	-	2.13	-		
		V _{GE} = 15 V, I _C = 50 A, T _J = 125 °C	-	2.76	-		
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 2 \text{ mA}$	4.6	5.8	7.6		
Temperature coefficient of threshold voltage	V _{GE(th)} /ΔT _J	V _{CE} = V _{GE} , I _C = 2 mA (25 °C to 125 °C)	-	-13	-	mV/°C	
Collector to emitter leakage current	I _{CES}	V _{GE} = 0 V, V _{CE} = 1200 V	- 1.7 50				
		$V_{GE} = 0 \text{ V}, V_{CE} = 1200 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	26.2	-	- μA	
Diode reverse breakdown voltage	V_{BR}	V _{BR} I _R = 1 mA		-	-	V	
		I _F = 25 A, V _{GE} = 0 V	-	2.11	2.42		
Diode forward voltage drop	V _{FM}	I _F = 50 A, V _{GE} = 0 V	-	2.72	-	V	
		I _F = 25 A, V _{GE} = 0 V, T _J = 125 °C	-	2.04	-		
		I _F = 50 A, V _{GE} = 0 V, T _J = 125 °C	-	2.83	-		
Disability and balance and	I _{RM}	V _R = 1200 V	-	4	50	μΑ	
Diode reverse leakage current		T _J = 125 °C, V _R = 1200 V	-	0.8	-	mA	
Gate to emitter leakage current	I _{GES}	I _{GES} V _{GE} = ± 20 V		-	± 200	nA	

SWITCHING CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg			-	171	-	
Gate to emitter charge (turn-on)	Q _{ge}	$I_C = 40 \text{ A}, V_{CC} = 960 \text{ V}, $	/ _{GE} = 15 V	-	22	-	nC
Gate to collector charge (turn-on)	Q_{gc}			-	86	-	
Turn-on switching loss	E _{on}	I _C = 50 A, V _{CC} = 600 V,		-	2.7	-	
Turn-off switching loss	E _{off}	$V_{GE} = 15 \text{ V}, R_{q} = 4.7 \Omega,$		-	1.4	-	
Total switching loss	E _{tot}	$L = 500 \mu H, T_J = 25 °C$		-	4.1	-	mJ
Turn-on switching loss	E _{on}		Energy losses include tail and diode recovery	-	4.1	-	
Turn-off switching loss	E _{off}	include t		-	2.3	-	
Total switching loss	E _{tot}			-	6.4	-	
Turn-on delay time	t _{d(on)}			-	8	-	
Rise time	t _r			-	11	-	
Turn-off delay time	t _{d(off)}			-	81	-	ns
Fall time	t _f			-	179	-	
Reverse bias safe operating area	RBSOA	T_J = 150 °C, I_C = 250 A, R_g = 4.7 Ω , V_{GE} = 15 V to 0 V, V_{CC} = 700 V, V_P = 1200 V			Fullsquare		
Diode reverse recovery time	t _{rr}			-	129	-	ns
Diode peak reverse current	I _{rr}	I _F = 50 A, dI _F /dt = 200 A/μs, V _R = 200 V		-	11	-	Α
Diode recovery charge	Q _{rr}			-	710	-	nC
Diode reverse recovery time	t _{rr}	$I_F = 50 \text{ A}, \text{ dI}_F/\text{dt} = 200 \text{ A/}\mu\text{s},$ $V_R = 200 \text{ V}, T_J = 125 ^{\circ}\text{C}$		-	208	-	ns
Diode peak reverse current	I _{rr}			-	17	-	Α
Diode recovery charge	Q _{rr}			-	1768	-	nC



THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction and storage temp	perature range	T _J , T _{Stg}		-40	-	150	°C
IGBT		В		-	-	0.43	
Junction to case —	Diode	R _{thJC}		-	-	0.37	°C/W
Case to heatsink		R _{thCS}	Flat, greased surface	-	0.05	-	
Weight				-	30	-	g
Mounting torque			Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
			Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				SOT-227			

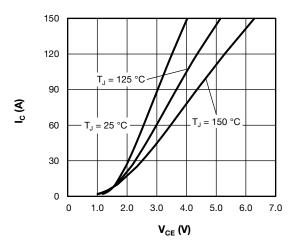


Fig. 1 - Typical Trench IGBT Output Characteristics, $V_{\text{GE}} = 15 \text{ V}$

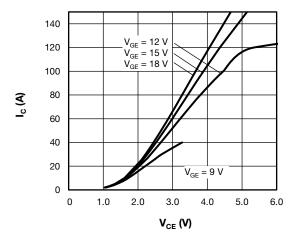


Fig. 2 - Typical Trench IGBT Output Characteristics, T_J = 125 $^{\circ}$ C

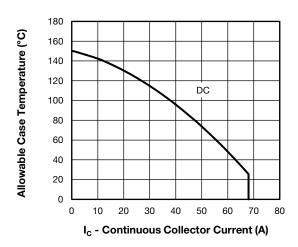


Fig. 3 - Maximum Trench IGBT Continuous Collector Current vs.

Case Temperature

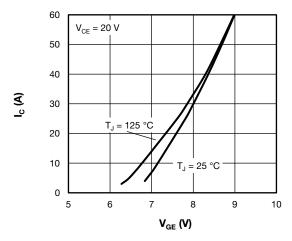


Fig. 4 - Typical Trench IGBT Transfer Characteristic



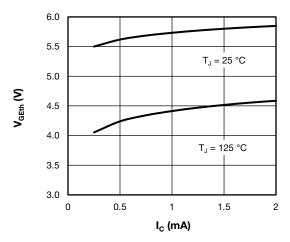


Fig. 5 - Typical Trench IGBT Gate Threshold Voltage

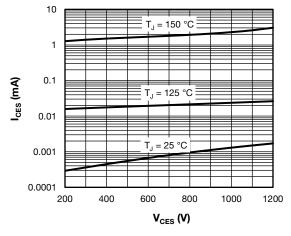


Fig. 6 - Typical Trench IGBT Zero Gate Voltage Collector Current

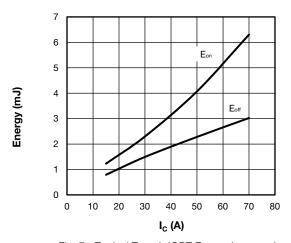


Fig. 7 - Typical Trench IGBT Energy Loss vs. I_C T_J = 125 °C, V_{CC} = 600 V, R_g = 4.7 Ω , V_{GE} = +15 V/-15 V, L = 500 μ H

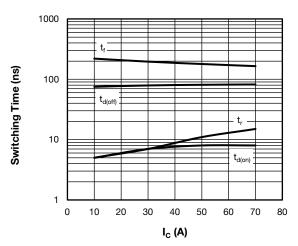


Fig. 8 - Typical Trench IGBT Switching Time vs. I_C T $_J$ = 125 °C, V $_{CC}$ = 600 V, R $_g$ = 4.7 $\Omega,$ V $_{GE}$ = +15 V/-15 V, L = 500 μH

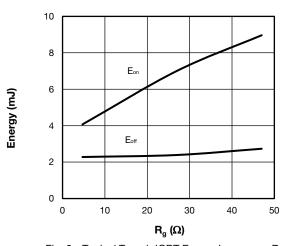


Fig. 9 - Typical Trench IGBT Energy Losses vs. R_g T_J = 125 °C, V_{CC} = 600 V, I_C = 50 A, V_{GE} = +15 V/-15 V, L = 500 μH

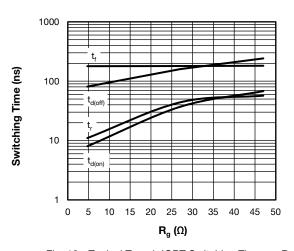


Fig. 10 - Typical Trench IGBT Switching Time vs. R_g T_J = 125 °C, V_{CC} = 600 V, I_C = 50 A, V_{GE} = +15 V/-15 V, L = 500 μH

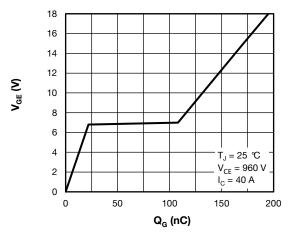


Fig. 11 - Typical Trench IGBT Gate Charge vs. Gate to Emitter Voltage

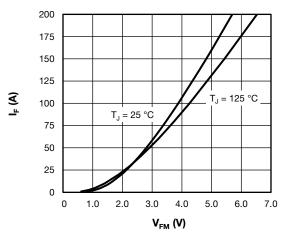


Fig. 12 - Typical Diode Forward Characteristic

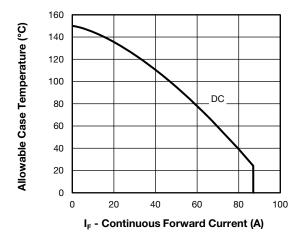


Fig. 13 - Maximum Diode Continuous Forward Current vs. Case Temperature

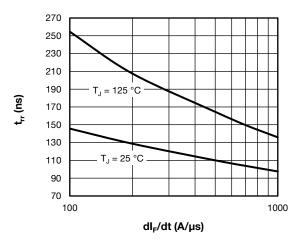


Fig. 14 - Typical Diode Reverse Recovery Time vs. dI_F/dt

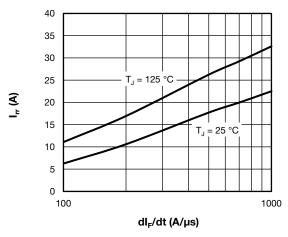


Fig. 15 - Typical Diode Reverse Recovery Current vs. dl_F/dt

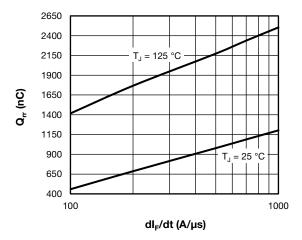


Fig. 16 - Typical Diode Reverse Recovery Charge vs. dI_{F}/dt



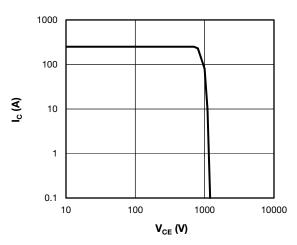


Fig. 17 - Trench IGBT Reverse BIAS SOA T $_J$ = 150 °C, I $_C$ = 250 A, R $_g$ = 4.7 $\Omega,$ V $_{GE}$ = +15 V/0 V, V $_{CC}$ = 700 V, V $_p$ = 1200 V

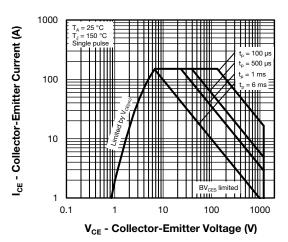


Fig. 18 - Trench IGBT Safe Operating Area

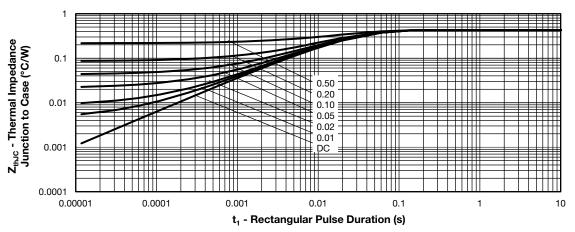


Fig. 19 - Maximum Trench IGBT Thermal Impedance Z_{thJC} Characteristics

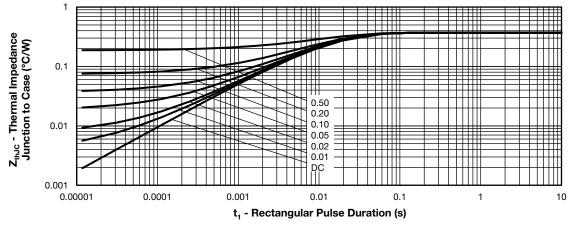


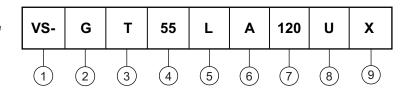
Fig. 20 - Maximum Diode Thermal Impedance Z_{thJC} Characteristics



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ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product
- Insulated gate bipolar transistor (IGBT)
- T = trench IGBT
- 4 Current rating (55 = 55 A)
- Circuit configuration (L = low side chopper)
- Package indicator (A = SOT-227)
- 7 Voltage rating (120 = 1200 V)
- Speed/type (U = ultrafast IGBT)
- Diode (X = HEXFRED® diode)

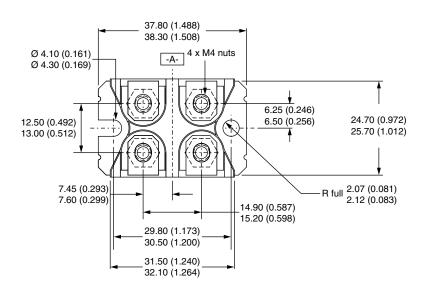
CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Low side chopper	L	30 20	Lead Assignment 4 1 2		

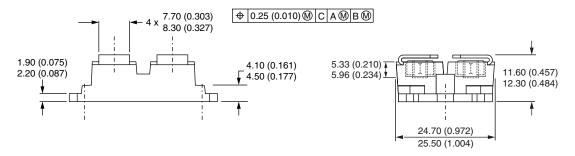
LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95423</u>					
Packaging information	www.vishay.com/doc?95425				

Vishay Semiconductors

SOT-227 Generation 2

DIMENSIONS in millimeters (inches)





Note

· Controlling dimension: millimeter



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