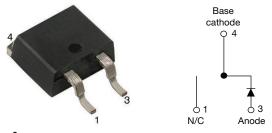
Vishay Semiconductors



650 V Gen 3 Power SiC Merged PIN Schottky Diode, 4 A



D²PAK 2L (TO-263AB 2L)

LINKS TO ADDITIONAL RESOURCES





PRIMARY CHARACTERISTICS								
I _{F(AV)} 4 A								
V _R	650 V							
V _F at I _F at 25 °C, typ.	1.3 V							
T _J max.	175 °C							
I _R at V _R at 175 °C	1.3 µA							
Q _C (V _R = 400 V)	12 nC							
Package	D ² PAK 2L (TO-263AB 2L)							
Circuit configuration	Single							

FEATURES

- Majority carrier diode using Schottky technology on SiC wide band gap material
- Improved V_F and efficiency by thin wafer ROHS technology
- Positive V_F temperature coefficient for easy paralleling
- Virtually no recovery tail and no switching losses
- Temperature invariant switching behavior
- 175 °C maximum operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 $^{\circ}\mathrm{C}$
- MPS structure for high ruggedness to forward current surge events
- Meets JESD 201 class 1A whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

Wide band gap SiC based 650 V Schottky diode, designed for high performance and ruggedness.

Optimum choice for high speed hard switching and efficient operation over a wide temperature range, it is also recommended for all applications suffering from Silicon ultrafast recovery behavior.

Typical applications include AC/DC PFC and DC/DC ultra high frequency output rectification in FBPS and LLC converters.

MECHANICAL DATA

Case: D²PAK 2L (TO-263AB 2L)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Peak repetitive reverse voltage	V _{RRM}		650	V				
Continuous forward current	۱ _F	I_F $T_C = 151 \ ^{\circ}C \ (DC)$		А				
Continuous forward current	I _F ⁽²⁾	T _C = 156 °C (DC)	- 4	А				
DC blocking voltage	V _{DC}		650	V				
Repetitive peak forward current	I _{FRM}	T_{C} = 25 °C, f = 50 Hz, square wave, DC = 25 %	21					
Non repetitive peak forward auroa auroat	I _{FSM}	$T_{C} = 25 \text{ °C}, t_{p} = 10 \text{ ms}, \text{ half sine wave}$	29	А				
Non-repetitive peak forward surge current		T _C = 110 °C, t _p = 10 ms, half sine wave	26]				
	P _{tot} ⁽¹⁾	T _C = 25 °C	40	w				
Power dissipation	Ftot ''	T _C = 110 °C	17	vv				
Fower dissipation	P _{tot} ⁽²⁾	T _C = 25 °C	52	14/				
	Ptot (=)	T _C = 110 °C	22	- W				
124		T _C = 25 °C	4.1	A2-				
l ² t value		T _C = 110 °C	3.3	A ² s				
Operating junction and storage temperatures	T _J ⁽³⁾ , T _{Stg}		-55 to +175	°C				

Notes

⁽¹⁾ Based on maximum R_{th}

⁽²⁾ Based on typical R_{th}

 $^{(3)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J < 1/R_{0JA}

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VS-3C04ET07S2L-M3

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ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)								
PARAMETER	MIN.	TYP.	MAX.	UNITS				
		$I_F = 4 A$	-	1.3	1.5	V		
Forward voltage	V _F	I _F = 4 A, T _J = 150 °C	-	1.5	1.85			
		I _F = 4 A, T _J = 175 °C	-	1.58	-			
	I _R	V _R = V _R rated	-	0.2	25	μΑ		
Reverse leakage current		$V_R = V_R$ rated, $T_J = 150 \ ^\circ C$	-	0.8	50			
		$V_{R} = V_{R}$ rated, $T_{J} = 175 \text{ °C}$	-	1.3	-			
Total capacitance	С	V _R = 1 V, f = 1 MHz	-	175	-	pF		
rotal capacitance	U	V _R = 400 V, f = 1 MHz	-	21	-	pr		
Total capacitive charge	Q _C	V _R = 400 V, f = 1 MHz	-	12	-	nC		

THERMAL - MECHANICAL SPECIFICATIONS (T _A = 25 °C unless otherwise specified)								
PARAMETER SYMBOL TEST CONDITIONS MIN. TYP. MAX. UN								
Thermal resistance, junction to case	R _{thJC}		-	2.9	3.8	°C/W		
Marking device	3C04ET07S							

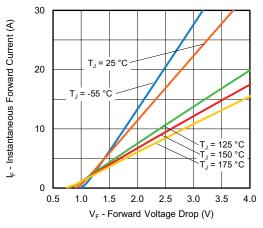


Fig. 1 - Typical Forward Voltage Drop Characteristics

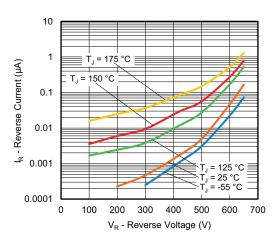


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

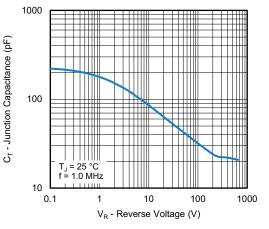


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

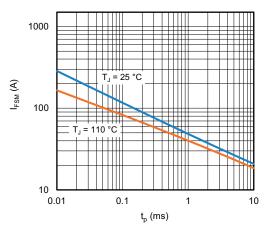
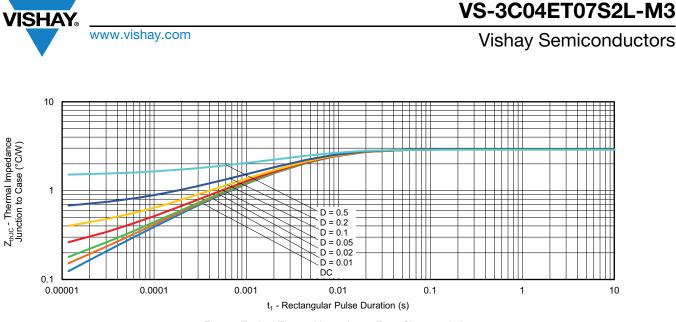


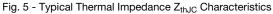
Fig. 4 - Non-Repetitive Peak Forward Surge Current vs. Pulse Duration (Square Wave)

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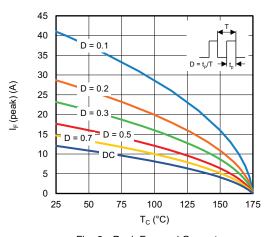


Fig. 6 - Peak Forward Current vs. Maximum Allowable Case Temperature

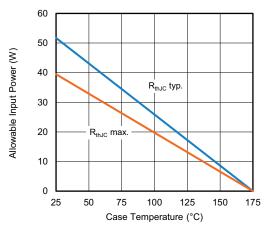


Fig. 7 - Forward Power Loss Characteristics

5 4 Capacitive Energy (µJ) 3 2 T_ = 25 °C 1 C V dV $E_{+} =$ 0 100 200 300 400 600 700 0 500 Reverse Voltage (V)

Fig. 8 - Typical Capacitive Energy vs. Reverse Voltage

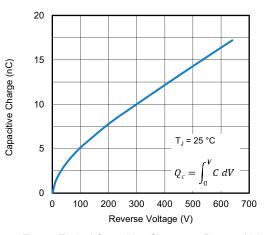


Fig. 9 - Typical Capacitive Charge vs. Reverse Voltage

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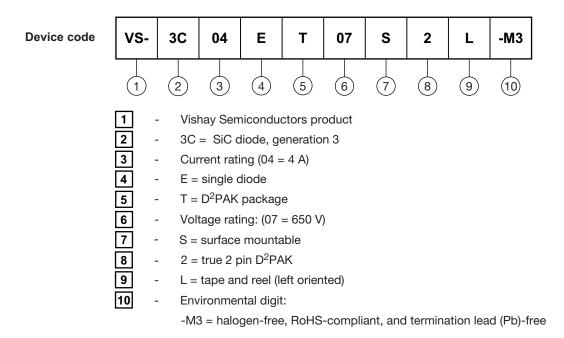
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ORDERING INFORMATION TABLE

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ORDERING INFORMATION							
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION					
VS-3C04ET07S2L-M3	800 per reel	13" diameter reel					

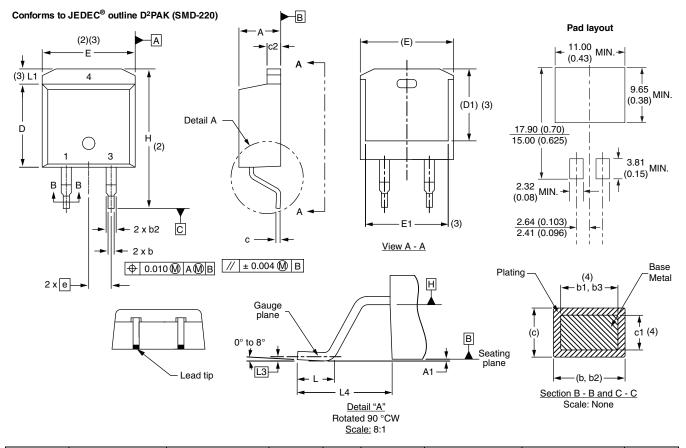
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?96683				
Part marking information	www.vishay.com/doc?96693				
Packaging information	www.vishay.com/doc?95032				

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D²PAK 2L (TO-263AB 2L)

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS	ETERS	INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L3 0.25 E		0.25 BSC		BSC	
c2	1.14	1.65	0.045	0.065			L4	4.78	5.28	0.188	0.208	
D	8.51	9.65	0.335	0.380	2							

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
 (3) Thermal and contain antional within dimension E 1.1, D1 and E1.

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

(7) Outline conforms to JEDEC® outline TO-263AB

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